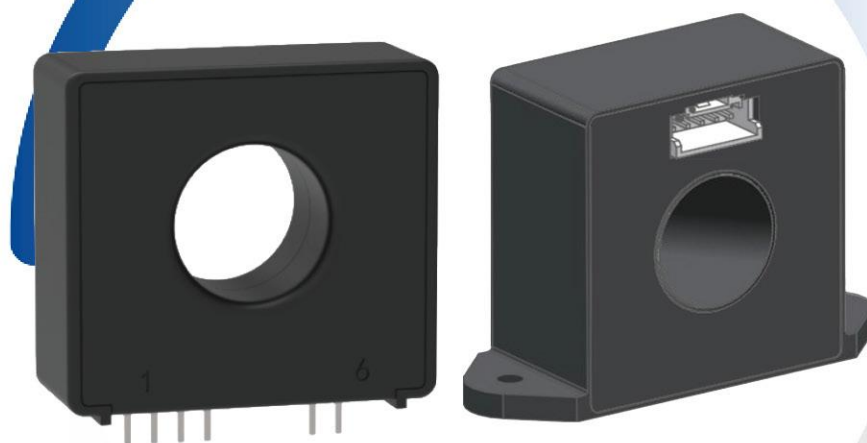


VRCM14/16

CURRENT MODULE

VeKON Technology
Secure your charging safety !



Features

Type-A+6mA residual current module for EV charging pile protection.

All-in-One highly-integrated digital output.

Standard

Applicable for IEC 62955-2018 residual current requirements for RDC-PD.

Applicable for GB/T 22794 and adapted to DC 6mA test.

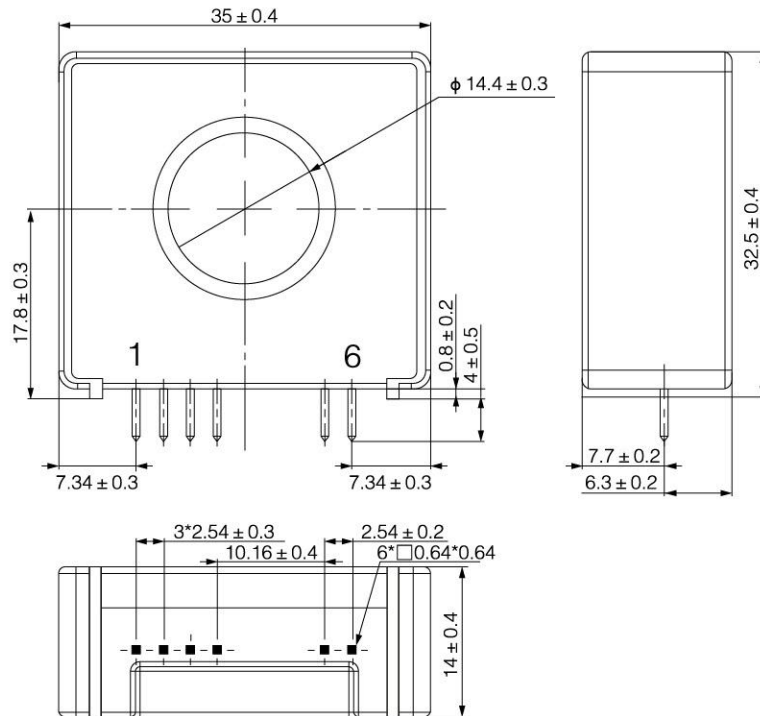
Trip-Current(Residual Current Related Characteristics)

Wav	Freq	Min	Typ	Max	Unit
AC	50Hz	20	22.8	26	mA
A0	50Hz	11	15	30	mA
A90	50Hz	10	18	30	mA
A135	50Hz	10	24	35	mA
2PDC	50Hz	3.5	5	7	mA
3PDC	-	3.1	4.6	6.2	mA
S-DC	-	3	4.6	6	mA
F	-	18	24	38	mA

VRCM14 CURRENT MODULE



Typical Application Schematic & Pin Definition



Pin-No.	Pin Name	Function
PIN-1	VDD	Module power supply, standard voltage 5VDC.
		Voltage input required to be within 4.85~5.15VDC, power output capability > 100mA.
		Power supply ripple ≤ 150 mV (It is suggested to use LDO circuit, for reference IC LP2985A-50DB).
PIN-2	TRIP	When tripping residual current detected by the module, this pin will output high-level voltage.
PIN-3	GND	Ground
PIN-4	CAL	When this pin been conducted to 0VDC, module will calculate the zero-point-drift and store the value to register in the MCU to finish the calibration operation.
		Attention: When using the TEST-IN function, the main circuit must be cut-off to ensure no residual current flow.
		When using this pin function, please follow the time diagram figure
PIN-5	TEST	Before charging, residual current test can be done via this pin with a high-level trigger, to verify if product function is OK.

VRCM16 CURRENT MODULE



Features

Type-A+6mA residual current module for EV charging pile protection.

All-in-One highly-integrated digital output.

Standard

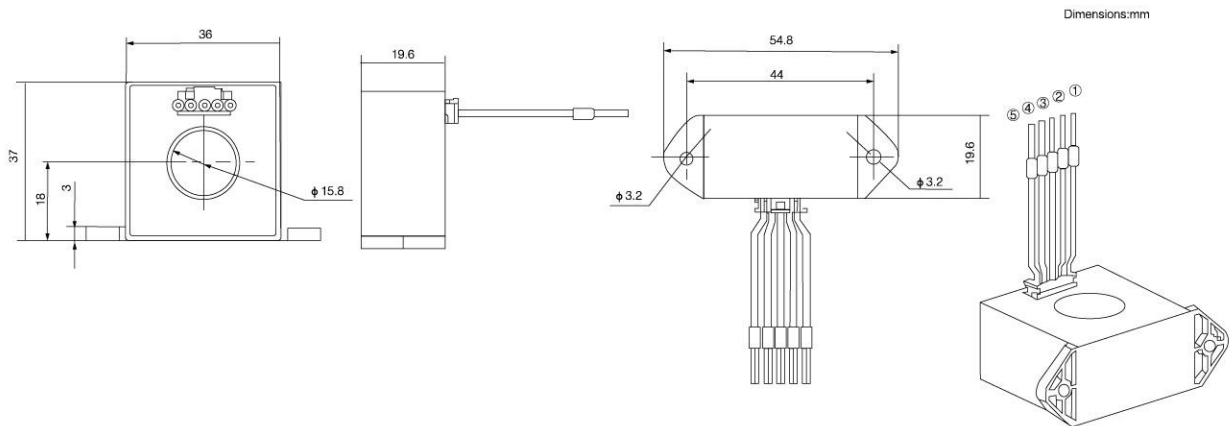
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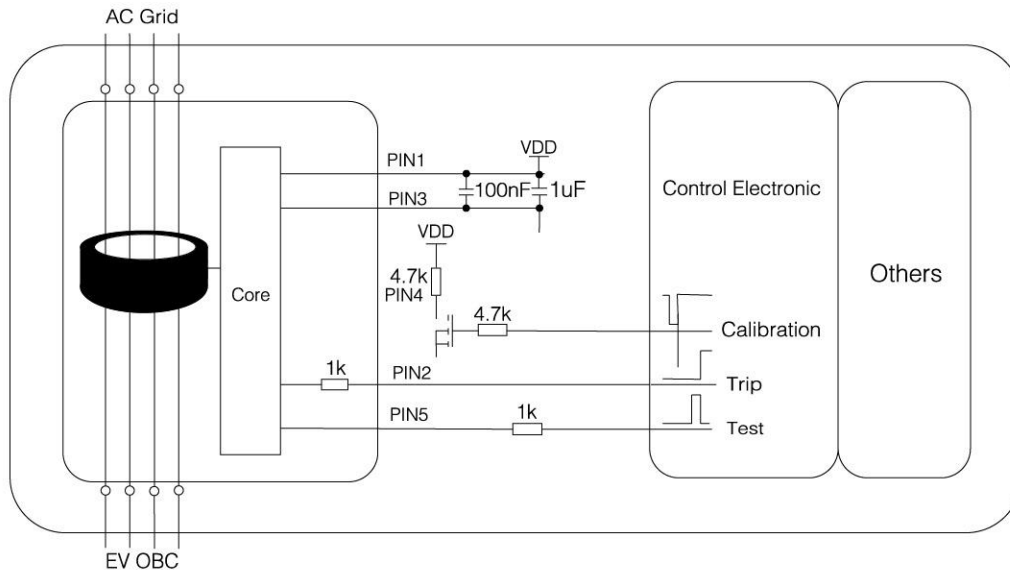
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3PDC	-	3.1	4.6	6.2	mA
S-DC	-	3	4.6	6	mA
F	-	18	24	38	mA

Product Dimension



VRCM16 CURRENT MODULE

Typical Application Schematic & Pin Definition



Pin-No.	Pin Name	Function
PIN-1	VDD	Module power supply,standard voltage 5VDC.
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		When using this pin function,please follow the time diagram figure
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