

主流网络安全通信平台

RA-N1601 产品规格书



编制：刘富刚

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日期：2018-06-26

Changelist

版本	日期	变更内容	修订人
V1.0	2018-06-26	首次发行	刘富刚

一、产品概述

RB-N1601 是锐宝智联开发的一款主流网络安全通信平台。整机采用 Intel 最新的 H110 芯片组，支持 Intel Skylake LGA1151 i7/i5/i3/Pentium/Celeron 系列 CPU。支持双通道 DDR4 2133 Non ECC 内存，具有 2 个 DDR4 DIMM 内存槽，最大支持 32G 内存。整机支持 1 个 CFast 接口,1 个 3.5 寸硬盘位。板载 6 个 RJ45 千兆电口，兼容 1 扩和 2 扩，最大可扩展 16 个千兆网口或 4 个万兆光口。可广泛应用于高端防火墙、VPN、IPS、IDS、流量控制等网络安全行业。

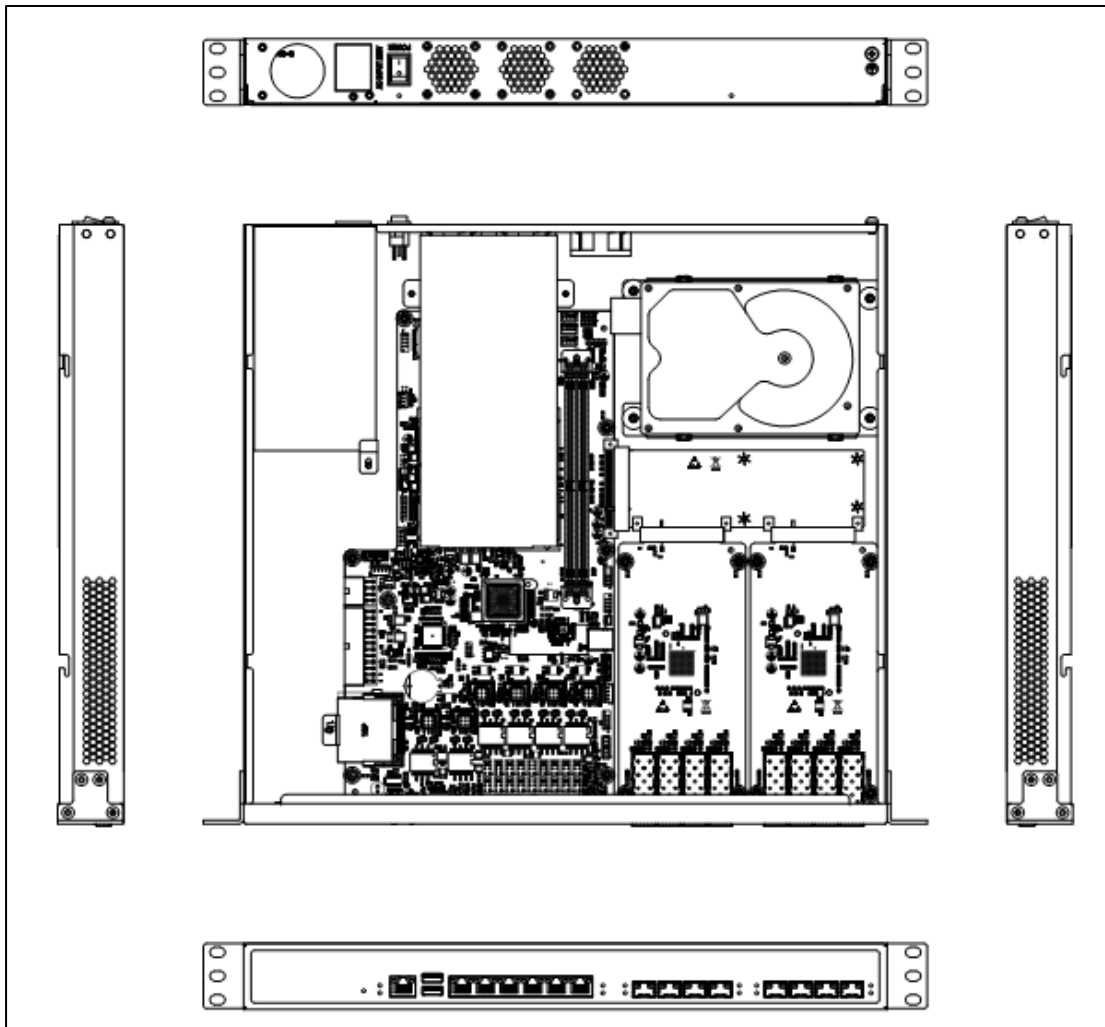
二、产品特点

1. 采用 Intel H110 芯片组
2. 支持 Intel Skylake i7/i5/i3/Pentium/Celeron 系列 CPU
3. 支持 2*DDR4 DIMM 内存槽，最大支持 32G
4. 板载 6 个 RJ45 千兆电口，2 组 Bypass，最大可扩展 16 个千兆网口

三、产品规格

序号	种类	项目	描述
1	平台	CPU	Intel Skylake i7/i5/i3/Pentium/Celeron
		芯片组	Intel H110
2	内存	类型	2X DDR4 2133 Non ECC
		插槽	2X 288pin DIMM
		容量	Max 32G
3	网络	网络接口	6X GE Ports (2XBypass)
		控制芯片	6X Intel i211
		扩展模块	见订购信息
4	存储	硬盘	2X SATA3.0
		CFast	1X CFast
5	扩展	PCIE	2X 卧式 PCIE X8 扩展槽，扩展网卡
6	I/O 接口	USB	2X USB3.0
		Console	1X RJ45 COM
		LAN	6X RJ45 千兆网口
		扩展	2X PCIE X8 扩展槽
		开关	1X Reset 开关
7	电源	单电	250W ATX
8	物理参数	尺寸	440X400X44mm (1U)
		温度	工作温度：-15~50℃；存储温度：-40~70℃
		湿度	5%-95%，无冷凝
9	系统	操作系统	Windows, Linux

四、产品布局图



五、订购信息

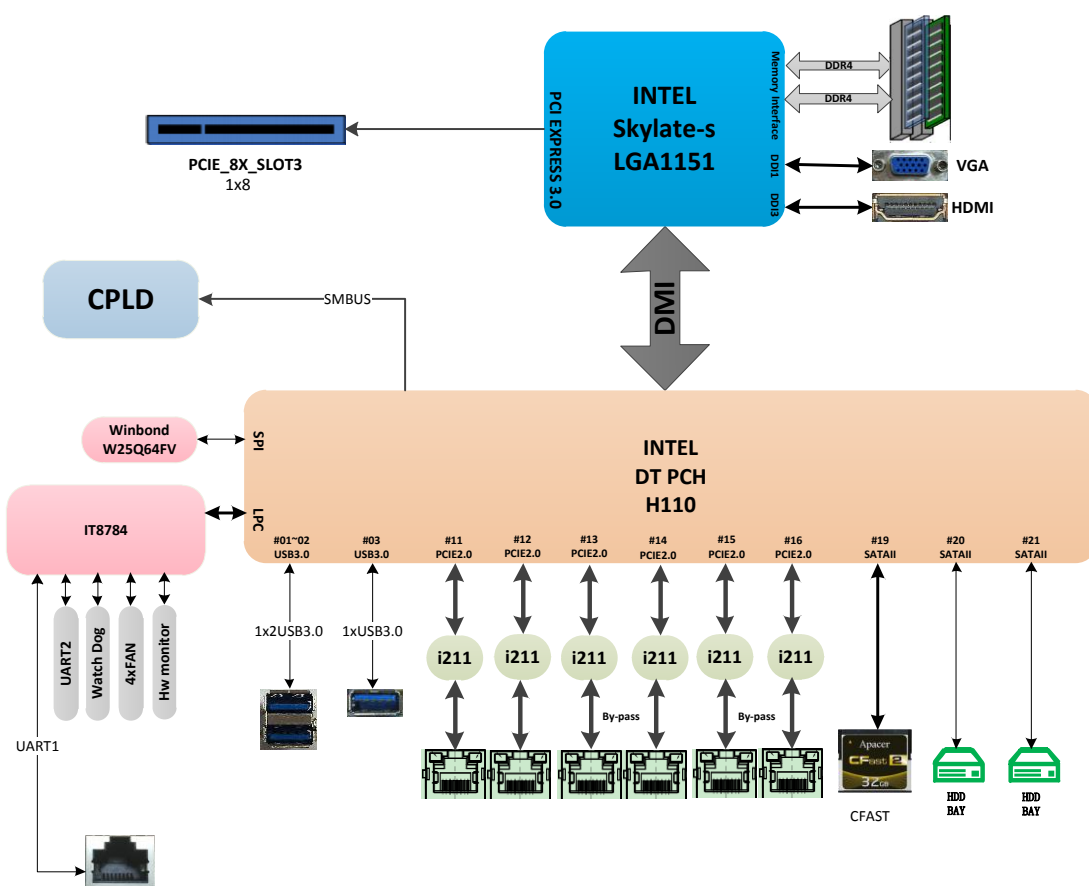
产品名称	简述
RA-N1601	1U 19" 上架机箱，6 千兆电口，250W ATX 电源，2 个扩展槽位

扩展部件

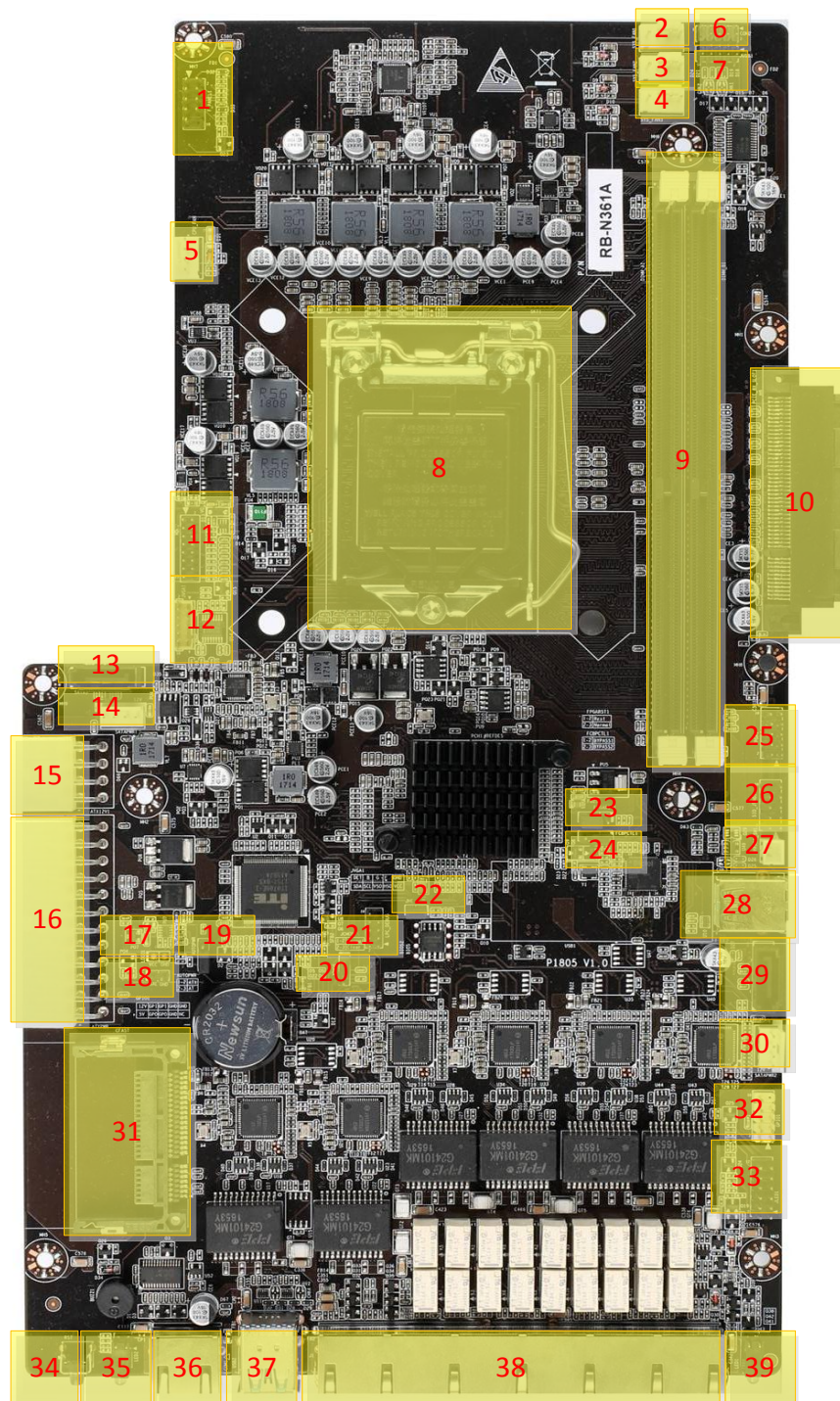
类别	型号	规格
处理器	I3-6100	2 核 4 线程，3M Cache，3.70 GHz
	I5-6500	4 核 4 线程，6M Cache，3.20 GHz
	I7-6700	4 核 8 线程，8M Cache，3.40 GHz
	RB-N317-4G	Intel i350-AM4, 4 GbE SFP

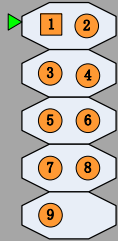
扩展卡	RB-N311-8D	Intel i350-AM4, 8 GbE RJ45, 4 pairs Gen3 Bypass
	RB-N312-8G	Intel i350-AM4, 8 GbE SFP, No Bypass
	RB-N313-2W	Intel 82599ES, 2X10GbE SFP+, No Bypass
	RB-N315-4DG	Intel i350-AM4, 4 GbE RJ45+4 GbE SFP, 2 pairs Gen3 Bypass
	RB-N316-4GB	Intel i350-AM4, 4 GbE SFP, 2 pairs Multi-mode Bypass

六、产品框图

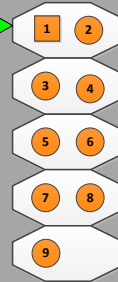


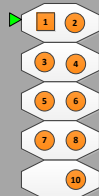
七、主板定义

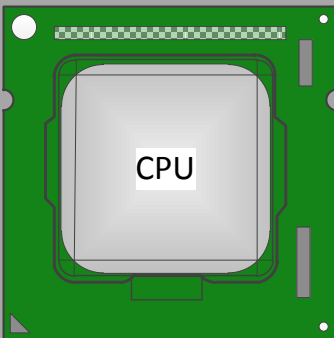


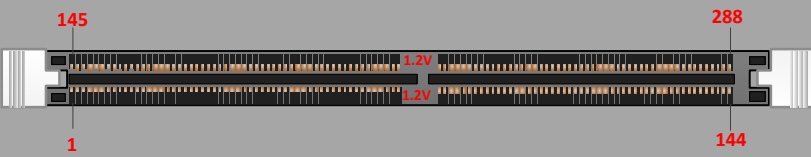
FPANEL	FPANEL(1)					
PIN Define		1	PWBTN+	2	HDD_LED+	
		3	PWBTN-	4	HDD_LED-	
		5	RESET+	6	PWR_LED+	
		7	RESET-	8	PWR_LED-	
		9	NULL	10	NC	
Type	2X5 Dupont Header PH=2.54 mm NC PIN10					

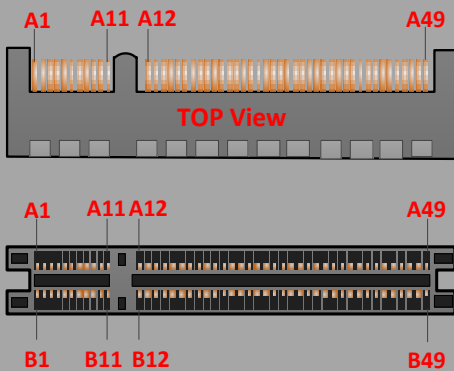
FAN	SYS_FAN1(2),SYS_FAN2(3),SYS_FAN3(4),CPU_FAN(5)					
PIN Define		1	GND			
		2	+V12_FAN			
		3	FAN_TAC			
		4	FAN_CTL			
Type	1X4 FAN Connector, PH=2.54mm					

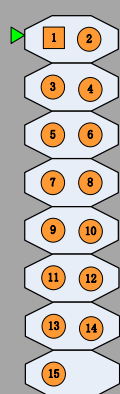
COM	COM2(6)					
PIN Define		1	DCD	2	RXD	
		3	TXD	4	DTR	
		5	GND	6	DSR	
		7	RTS	8	CTS	
		9	RI			
Type	2x5 DuPont Header,PIN10 NC, PH=2.0mm					

JUSB	JUSB1(7)																							
PIN Define		<table><tr><td>1</td><td>VBus</td><td>2</td><td>VBus</td></tr><tr><td>3</td><td>D0-</td><td>4</td><td>D1-</td></tr><tr><td>5</td><td>D0+</td><td>6</td><td>D2+</td></tr><tr><td>7</td><td>GND</td><td>8</td><td>GND</td></tr><tr><td>9</td><td>NC</td><td>10</td><td>GND</td></tr></table>	1	VBus	2	VBus	3	D0-	4	D1-	5	D0+	6	D2+	7	GND	8	GND	9	NC	10	GND		
1	VBus	2	VBus																					
3	D0-	4	D1-																					
5	D0+	6	D2+																					
7	GND	8	GND																					
9	NC	10	GND																					
Type	2x5 DuPont Header,PIN9 NC, PH=2.54mm																							

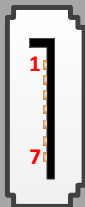
CPU	CPU(8)	
PIN Define		
Type	Skylake-S LGA1151	

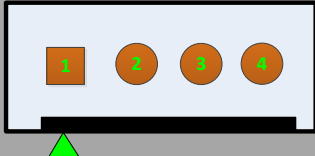
DIMM	DIMM_A1 ,DIMM_B1 (9)	
PIN Define		
Type	Standard DDR4 UDIMM Socket	

PCIE8X	PCIE_8X_SLOT2(10)
PIN Define	 TOP View
Type	Standard Splint PCIE8X,PIN Define is Special, not Generic

HDMI	HDMI(1)				
PIN Define		1	HPD	2	GND
		3	+5V	4	GND
		5	SCL	6	SDA
		7	CLK_N	8	CLK_P
		9	TX0_N	10	TX0_P
		11	TX1_N	12	TX1_P
		13	TX2_N	14	TX2_P
		15	GND	16	NC
Type	2X8 Dupont Header PH=2.0 mm				

JVGA1	VGA header Pin define(12)																							
PIN Define	 <table><tr><td>1</td><td>GND</td><td>2</td><td>SDA</td></tr><tr><td>3</td><td>B</td><td>4</td><td>SCK</td></tr><tr><td>5</td><td>G</td><td>6</td><td>VSO</td></tr><tr><td>7</td><td>R</td><td>8</td><td>HSO</td></tr><tr><td>9</td><td>GND</td><td>10</td><td>Vcc</td></tr></table>				1	GND	2	SDA	3	B	4	SCK	5	G	6	VSO	7	R	8	HSO	9	GND	10	Vcc
1	GND	2	SDA																					
3	B	4	SCK																					
5	G	6	VSO																					
7	R	8	HSO																					
9	GND	10	Vcc																					
Type	2X5 Header Box PH=2.0 mm																							

SATA	SATA1 (13) , SATA2 (29)		
PIN Define		1	GND
		2	TX+
		3	TX-
		4	GND
		5	RX-
		6	RX+
		7	GND
Type	7PIN SATA		

SATA_PWR	SATA_PWR1(14),SATA_PWR2(30),									
PIN Define		<table><tr><td>1</td><td>+12V</td></tr><tr><td>2</td><td>GND</td></tr><tr><td>3</td><td>GND</td></tr><tr><td>4</td><td>+5V</td></tr></table>	1	+12V	2	GND	3	GND	4	+5V
1	+12V									
2	GND									
3	GND									
4	+5V									
Type	1X4 FDD Power Connector , PH=2.54mm									

ATX12V	ATX12V(15)				
PIN Define	 <table border="1" data-bbox="1053 448 1264 537"> <tr> <td>1-4</td><td>+12V</td></tr> <tr> <td>5-8</td><td>GND</td></tr> </table>	1-4	+12V	5-8	GND
1-4	+12V				
5-8	GND				
Type	ATX Standard 8pin connector				

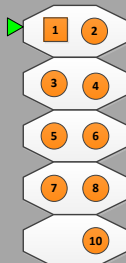
ATXPWR	ATXPWR (16)																																																
PIN Define	1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24																																																
	<table><tr><td>1</td><td>3.3V</td><td>13</td><td>3.3V</td></tr><tr><td>2</td><td>3.3V</td><td>14</td><td>-12V</td></tr><tr><td>3</td><td>GND</td><td>15</td><td>GND</td></tr><tr><td>4</td><td>5V</td><td>16</td><td>PSON</td></tr><tr><td>5</td><td>GND</td><td>17</td><td>GND</td></tr><tr><td>6</td><td>5V</td><td>18</td><td>GND</td></tr><tr><td>7</td><td>GND</td><td>19</td><td>GND</td></tr><tr><td>8</td><td>PG</td><td>20</td><td>-5V</td></tr><tr><td>9</td><td>5VSB</td><td>21</td><td>5V</td></tr><tr><td>10</td><td>12V</td><td>22</td><td>5V</td></tr><tr><td>11</td><td>12V</td><td>23</td><td>5V</td></tr><tr><td>12</td><td>3.3V</td><td>24</td><td>GND</td></tr></table>	1	3.3V	13	3.3V	2	3.3V	14	-12V	3	GND	15	GND	4	5V	16	PSON	5	GND	17	GND	6	5V	18	GND	7	GND	19	GND	8	PG	20	-5V	9	5VSB	21	5V	10	12V	22	5V	11	12V	23	5V	12	3.3V	24	GND
	1	3.3V	13	3.3V																																													
	2	3.3V	14	-12V																																													
	3	GND	15	GND																																													
	4	5V	16	PSON																																													
	5	GND	17	GND																																													
	6	5V	18	GND																																													
	7	GND	19	GND																																													
	8	PG	20	-5V																																													
	9	5VSB	21	5V																																													
	10	12V	22	5V																																													
	11	12V	23	5V																																													
12	3.3V	24	GND																																														
Type	ATX Customized 24 pin connector																																																
Memo	Support standard ATX Power																																																

JLOGO	LOGO(17)				
PIN Define	 <table border="1" data-bbox="1021 1758 1212 1848"> <tr> <td>1</td> <td>VCC</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> </table>	1	VCC	2	GND
1	VCC				
2	GND				
Type	1x2 DuPont Header PH=2.54mm				
Memo	LOGO BOARD Power supply				

TEST	TEST(18)				
PIN Define	 <table border="1"> <tr> <td>1</td><td>+V5AL</td></tr> <tr> <td>2</td><td>GND</td></tr> </table>	1	+V5AL	2	GND
1	+V5AL				
2	GND				
Type	1x2 DuPont Header PH=2.54mm				
Memo	For TEST Only				

AUTOPWR	AUTOPWR(19)											
PIN Define		<table><tr><th>Pin</th><th>1-2</th><th>2-3</th></tr><tr><td>Define</td><td>ATX</td><td>AT</td></tr><tr><td>Default</td><td colspan="2">1-2</td></tr></table>	Pin	1-2	2-3	Define	ATX	AT	Default	1-2		
Pin	1-2	2-3										
Define	ATX	AT										
Default	1-2											
Type	1x3 DuPont Header PH=2.54mm											
Memo	This Setting is independent from Bios Power State setting											

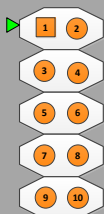
JCMOS	JCMOS1(20)									
PIN Define	 <table><tr><th>Pin</th><th>1-2</th><th>2-3</th></tr><tr><td>Define</td><td>Normal</td><td>Clear</td></tr><tr><td>Default</td><td colspan="2">1-2</td></tr></table>	Pin	1-2	2-3	Define	Normal	Clear	Default	1-2	
Pin	1-2	2-3								
Define	Normal	Clear								
Default	1-2									
Type	1x3 DuPont Header PH=2.54mm									
Memo	Short 2-3,Clear CMOS									

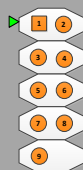
LPC_DEBUG	LPC_DEBUG(21)																				
PIN Define	 <table> <tr> <td>1</td> <td>LAD3</td> <td>2</td> <td>25M_CLK</td> </tr> <tr> <td>3</td> <td>LAD2</td> <td>4</td> <td>LFRAME#</td> </tr> <tr> <td>5</td> <td>LAD1</td> <td>6</td> <td>LESET#</td> </tr> <tr> <td>7</td> <td>LAD0</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>NC</td> <td>10</td> <td>+3.3V</td> </tr> </table>	1	LAD3	2	25M_CLK	3	LAD2	4	LFRAME#	5	LAD1	6	LESET#	7	LAD0	8	GND	9	NC	10	+3.3V
1	LAD3	2	25M_CLK																		
3	LAD2	4	LFRAME#																		
5	LAD1	6	LESET#																		
7	LAD0	8	GND																		
9	NC	10	+3.3V																		
Type	2x5 DuPont Header,PIN9 NC，PH=2.0mm																				
Memo	For Debug only																				

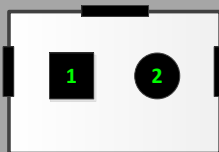
JME	JME(22)
PIN Define	
Type	1x2 DuPont Header PH=2.54mm
Memo	Short 1-2 can Disable ME

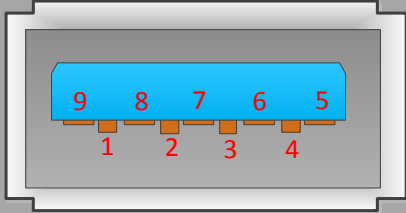
FPGARST	FPGARST1 (23)			
PIN Define		Pin	1-2	2-3
		Define	FPGA_RST	
		Default	1-2	
Type	1x3 DuPont Header PH=2.54mm			
Memo	Short PIN1-2 Reset FPGA			

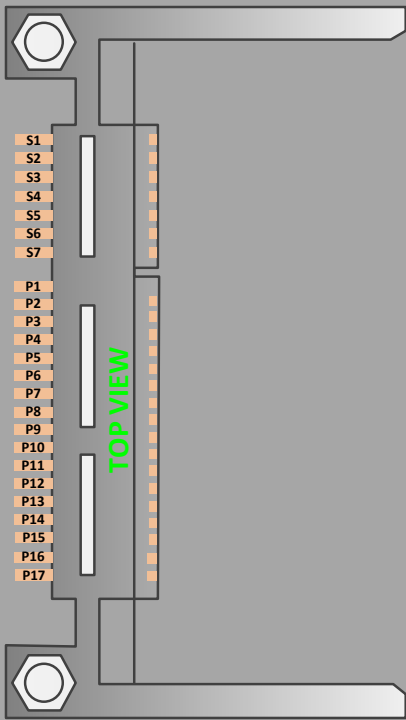
FPGABPCTL	FPGABPCTL1 (24)									
PIN Define	 <table><tr><th>Pin</th><th>1-2</th><th>2-3</th></tr><tr><td>Define</td><td>Bypass1</td><td>Bypass2</td></tr><tr><td>Default</td><td colspan="2">1-2</td></tr></table>	Pin	1-2	2-3	Define	Bypass1	Bypass2	Default	1-2	
Pin	1-2	2-3								
Define	Bypass1	Bypass2								
Default	1-2									
Type	1x3 DuPont Header PH=2.54mm									
Memo	Set for different kind of bypass									

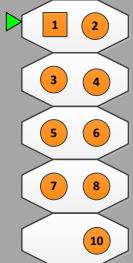
FPGA	FPGA1(25)																							
PIN Define		<table><tr><td>1</td><td>PCIE16X_TRST</td><td>2</td><td>FPGA_TRST</td></tr><tr><td>3</td><td>PCIE16X_TDO</td><td>4</td><td>FPGA_TDO</td></tr><tr><td>5</td><td>PCIE16X_TMS</td><td>6</td><td>FPGA_TMS</td></tr><tr><td>7</td><td>PCIE16X_TDI</td><td>8</td><td>FPGA_TDI</td></tr><tr><td>9</td><td>PCIE16X_TCK</td><td>10</td><td>FPGA_TCK</td></tr></table>	1	PCIE16X_TRST	2	FPGA_TRST	3	PCIE16X_TDO	4	FPGA_TDO	5	PCIE16X_TMS	6	FPGA_TMS	7	PCIE16X_TDI	8	FPGA_TDI	9	PCIE16X_TCK	10	FPGA_TCK		
1	PCIE16X_TRST	2	FPGA_TRST																					
3	PCIE16X_TDO	4	FPGA_TDO																					
5	PCIE16X_TMS	6	FPGA_TMS																					
7	PCIE16X_TDI	8	FPGA_TDI																					
9	PCIE16X_TCK	10	FPGA_TCK																					
Type	2x5 DuPont Header, PH=2.54mm																							
Memo	Short 1-2,3-4,5-6,7-8,9-10,Use Dos program to update FPGA																							

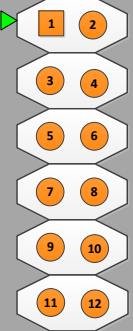
SIO_JTAG1	SIO_JTAG1 (26)				
PIN Define		1	FPGA_TCK	2	GND
		3	FPGA_TDO	4	NULL
		5	FPGA_TMS	6	+V3.3AL
		7	+V3.3AL	8	FPGA_TRST
		9	FPGA_TDI		
Type	2x5 DuPont Header, PH=2.54mm				

BYPASS_SW	BYPASS_SW1 (27)									
PIN Define		<table><tr><th colspan="2">SPK1</th></tr><tr><th>PIN</th><th>Assignment</th></tr><tr><td>1</td><td>ForceBypass</td></tr><tr><td>2</td><td>GND</td></tr></table>	SPK1		PIN	Assignment	1	ForceBypass	2	GND
SPK1										
PIN	Assignment									
1	ForceBypass									
2	GND									
Type	1x2 Wafer PH=2.0mm									
Memo	Short PIN1-2,Bypass status change as Bypass->break->bypass									

USB	USB1(28)																						
PIN Define	 <table border="1" data-bbox="935 477 1326 696"> <tr> <td>1</td><td>Vbus</td><td>2</td><td>D1-</td></tr> <tr> <td>3</td><td>D1+</td><td>4</td><td>GND</td></tr> <tr> <td>5</td><td>SSRX1-</td><td>6</td><td>SSRX1+</td></tr> <tr> <td>7</td><td>GND</td><td>8</td><td>SSTX1-</td></tr> <tr> <td>9</td><td>SSTX1+</td><td></td><td></td></tr> </table>			1	Vbus	2	D1-	3	D1+	4	GND	5	SSRX1-	6	SSRX1+	7	GND	8	SSTX1-	9	SSTX1+		
1	Vbus	2	D1-																				
3	D1+	4	GND																				
5	SSRX1-	6	SSRX1+																				
7	GND	8	SSTX1-																				
9	SSTX1+																						
Type	Standard Single 90 USB3.0 connector																						

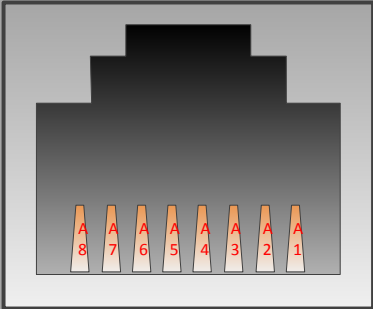
CFAST	CFAST(31)																																																																						
PIN Define	 <table border="1" data-bbox="895 936 1353 1671"> <tr> <td>P1</td><td>CDI</td><td>S1</td><td>GND</td></tr> <tr> <td>P2</td><td>GND</td><td>S2</td><td>RX+</td></tr> <tr> <td>P3</td><td>NC</td><td>S3</td><td>RX-</td></tr> <tr> <td>P4</td><td>NC</td><td>S4</td><td>GND</td></tr> <tr> <td>P5</td><td>NC</td><td>S5</td><td>TX-</td></tr> <tr> <td>P6</td><td>NC</td><td>S6</td><td>TX+</td></tr> <tr> <td>P7</td><td>GND</td><td>S7</td><td>GND</td></tr> <tr> <td>P8</td><td>NC</td><td></td><td></td></tr> <tr> <td>P9</td><td>NC</td><td></td><td></td></tr> <tr> <td>P10</td><td>NC</td><td></td><td></td></tr> <tr> <td>P11</td><td>NC</td><td></td><td></td></tr> <tr> <td>P12</td><td>NC</td><td></td><td></td></tr> <tr> <td>P13</td><td>+V3.3S</td><td></td><td></td></tr> <tr> <td>P14</td><td>+V3.3S</td><td></td><td></td></tr> <tr> <td>P15</td><td>GND</td><td></td><td></td></tr> <tr> <td>P16</td><td>GND</td><td></td><td></td></tr> <tr> <td>P17</td><td>NC</td><td></td><td></td></tr> </table>			P1	CDI	S1	GND	P2	GND	S2	RX+	P3	NC	S3	RX-	P4	NC	S4	GND	P5	NC	S5	TX-	P6	NC	S6	TX+	P7	GND	S7	GND	P8	NC			P9	NC			P10	NC			P11	NC			P12	NC			P13	+V3.3S			P14	+V3.3S			P15	GND			P16	GND			P17	NC		
P1	CDI	S1	GND																																																																				
P2	GND	S2	RX+																																																																				
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P6	NC	S6	TX+																																																																				
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Type	CFAST Socket																																																																						

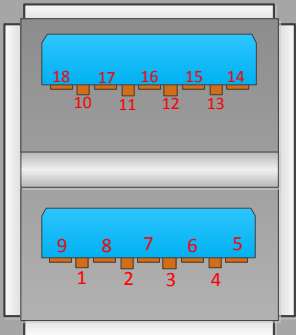
GPIO	GPIO(32)				
PIN Define		1	+5V	2	+12V
		3	GPIO1	4	GPIO2
		5	GPIO3	6	GPIO4
		7	GND	8	GND
		9		10	GND
Type	2X5 Header Box PH=2.54 mm				
Memo	Support 4 independent GPIO				

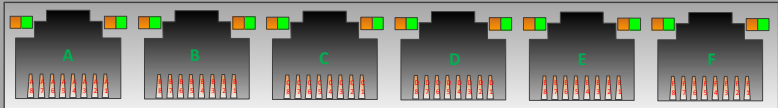
LED	JLED1(33)				
PIN Define		1	Vcc	2	HDD_LED-
		3	RUN_LED+	4	RUN_LED-
		5	HA_LED+	6	HA_LED-
		7	BP_LED+	8	BP_LED-
		9	Vcc	10	ALARM-
		11	Vcc	12	GND
Type	2X6 Header Box PH=2.54 mm				
Memo	Support LED				

RST	RST(34)				
PIN Define					
Type	Switch				
Memo	Press this switch will reset system				

LED	LED2 (35)								
PIN Define	1  2 	<table><tr><th colspan="2">LED2</th></tr><tr><td>1</td><td>PWR_LED(Green)</td></tr><tr><td>2</td><td>HDD_LED(Orange)</td></tr></table>		LED2		1	PWR_LED(Green)	2	HDD_LED(Orange)
LED2									
1	PWR_LED(Green)								
2	HDD_LED(Orange)								
Type	1x2 LED Green/Orange								
Memo	1:Indicate Power Status 2:Indicate HDD Status								

COM	COM1(36)		
PIN Define			COM1
		A1	RTS
		A2	DTR
		A3	SOUT
		A4	GND
		A5	GND
		A6	SIN
		A7	DSR
		A8	CTS
Type	Standard 1x1RJ45 Connector Without LED		

USB3.0	USB2(37)			
PIN Define				
	1	Vbus	2	D1-
	3	D1+	4	GND
	5	SSRX1-	6	SSRX1+
	7	GND	8	SSTX1-
	9	SSTX1+	10	Vbus
	11	D2-	12	D2+
	13	GND	14	SSRX2-
	15	SSRX2+	16	GND
	17	SSTX2-	18	SSTX2+
Type	Standard Dual USB3.0 connector			

LAN	J1(38)																																																																																																
PIN Define																																																																																																	
	<table><tr><td>A1</td><td>A+</td><td>B1</td><td>A+</td><td>C1</td><td>A+</td><td>D1</td><td>A+</td><td>E1</td><td>A+</td><td>F1</td><td>A+</td></tr><tr><td>A2</td><td>A-</td><td>B2</td><td>A-</td><td>C2</td><td>A-</td><td>D2</td><td>A-</td><td>E2</td><td>A-</td><td>F2</td><td>A-</td></tr><tr><td>A3</td><td>B+</td><td>B3</td><td>B+</td><td>C3</td><td>B+</td><td>D3</td><td>B+</td><td>E3</td><td>B+</td><td>F3</td><td>B+</td></tr><tr><td>A4</td><td>C+</td><td>B4</td><td>C+</td><td>C4</td><td>C+</td><td>D4</td><td>C+</td><td>E4</td><td>C+</td><td>F4</td><td>C+</td></tr><tr><td>A5</td><td>C-</td><td>B5</td><td>C-</td><td>C5</td><td>C-</td><td>D5</td><td>C-</td><td>E5</td><td>C-</td><td>F5</td><td>C-</td></tr><tr><td>A6</td><td>B-</td><td>B6</td><td>B-</td><td>C6</td><td>B-</td><td>D6</td><td>B-</td><td>E6</td><td>B-</td><td>F6</td><td>B-</td></tr><tr><td>A7</td><td>D+</td><td>B7</td><td>D+</td><td>C7</td><td>D+</td><td>D7</td><td>D+</td><td>E7</td><td>D+</td><td>F7</td><td>D+</td></tr><tr><td>A8</td><td>D-</td><td>B8</td><td>D-</td><td>C8</td><td>D-</td><td>D8</td><td>D-</td><td>E8</td><td>D-</td><td>F8</td><td>D-</td></tr></table>	A1	A+	B1	A+	C1	A+	D1	A+	E1	A+	F1	A+	A2	A-	B2	A-	C2	A-	D2	A-	E2	A-	F2	A-	A3	B+	B3	B+	C3	B+	D3	B+	E3	B+	F3	B+	A4	C+	B4	C+	C4	C+	D4	C+	E4	C+	F4	C+	A5	C-	B5	C-	C5	C-	D5	C-	E5	C-	F5	C-	A6	B-	B6	B-	C6	B-	D6	B-	E6	B-	F6	B-	A7	D+	B7	D+	C7	D+	D7	D+	E7	D+	F7	D+	A8	D-	B8	D-	C8	D-	D8	D-	E8	D-	F8	D-
	A1	A+	B1	A+	C1	A+	D1	A+	E1	A+	F1	A+																																																																																					
	A2	A-	B2	A-	C2	A-	D2	A-	E2	A-	F2	A-																																																																																					
	A3	B+	B3	B+	C3	B+	D3	B+	E3	B+	F3	B+																																																																																					
	A4	C+	B4	C+	C4	C+	D4	C+	E4	C+	F4	C+																																																																																					
	A5	C-	B5	C-	C5	C-	D5	C-	E5	C-	F5	C-																																																																																					
	A6	B-	B6	B-	C6	B-	D6	B-	E6	B-	F6	B-																																																																																					
	A7	D+	B7	D+	C7	D+	D7	D+	E7	D+	F7	D+																																																																																					
A8	D-	B8	D-	C8	D-	D8	D-	E8	D-	F8	D-																																																																																						
Type	Standard 1x6 RJ45 Connector With Yellow/Green Dual LED																																																																																																
Memo	C-D and E-F supports bypass																																																																																																

LED	LED1 (39)						
PIN Define	 <table border="1"> <thead> <tr><th colspan="2">LED1</th></tr> </thead> <tbody> <tr><td>1</td><td>LAN3-LAN4 Bypass status</td></tr> <tr><td>2</td><td>LAN5-LAN6 Bypass status</td></tr> </tbody> </table>	LED1		1	LAN3-LAN4 Bypass status	2	LAN5-LAN6 Bypass status
LED1							
1	LAN3-LAN4 Bypass status						
2	LAN5-LAN6 Bypass status						
Type	1x2 LED Green/Orange						
Memo	1:Indicate LAN3-LAN4 Bypass status(Orange Bypass/Green break) 2:Indicate LAN5-LAN6 Bypass status(Orange Bypass/Green break)						