

Product Spec

Motherboard

RB-F208M V1.1

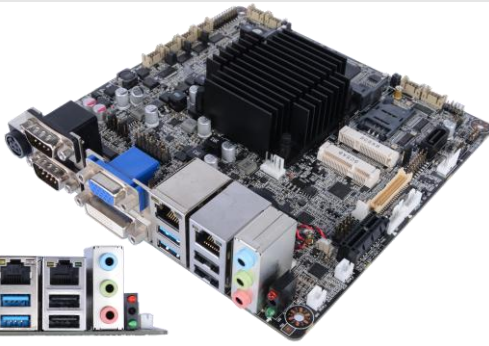
Prepared by : Ken

Date: 2017/07/10

Approved by: Peter

Date: 2017/07/10

Revision History			
Revision	Date	Description	Author
V1.0	2016/05/27	First release	KEN
V1.1	2017/07/10	Optimize	KEN



Features

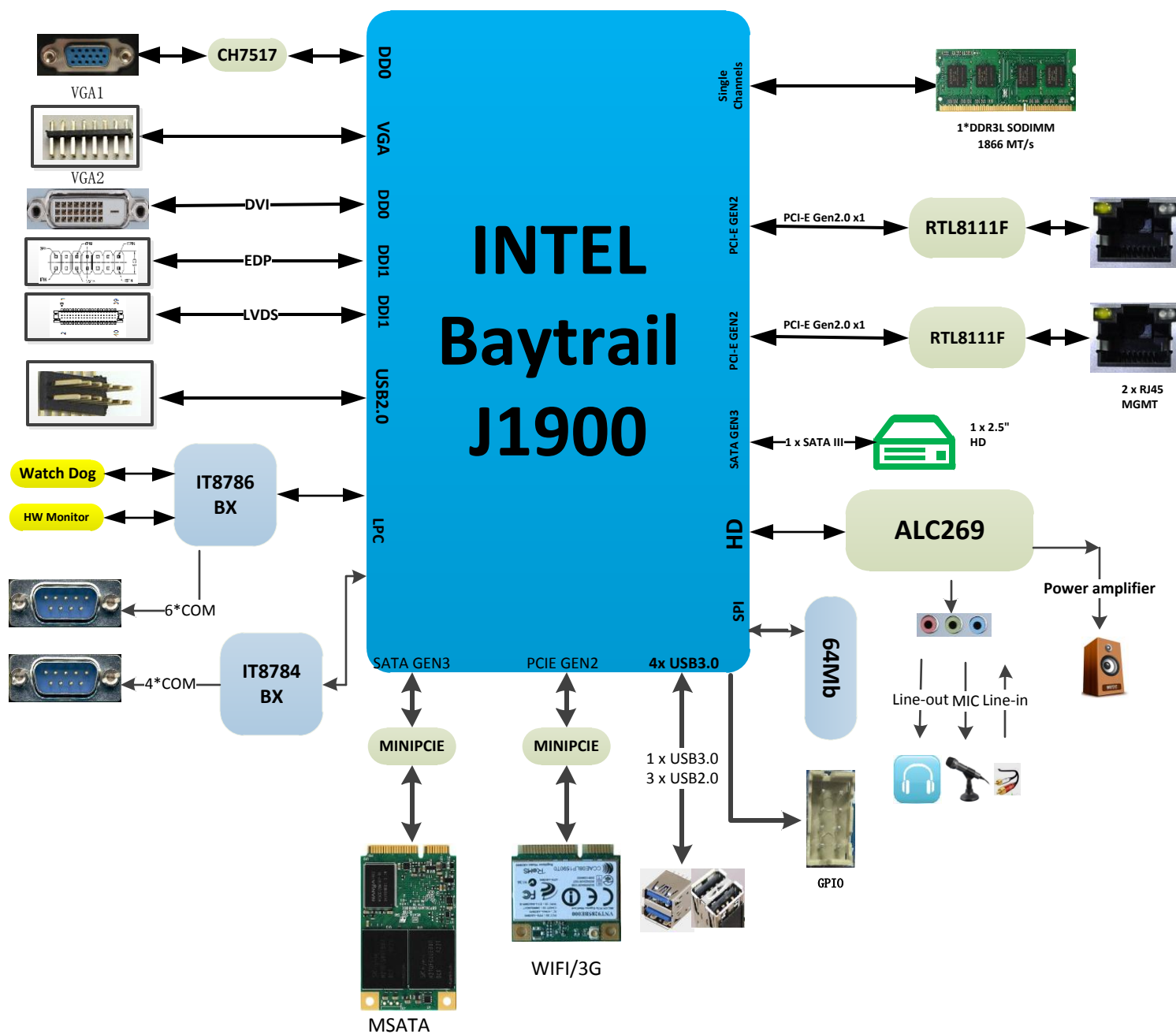
- *Intel Baytrail Platform, J1900 CPU, 2.0GHz, 4*Cores
- *Supports 2 independent Display,
- *Supports 2 X 10/100/1000M LAN adapter
- *Support 11 X USB2.0, 1 X USB3.0
- *Support 10 X COMs, 1 X SATAII, 1 X MSATA, 1 X WIFI/4G LTE, 1xPCIEX1
- *Support HD Audio, 1 X Audio out, 1 X Audio in, 1X MIC, 4ohm & 2w Power amplifier
- *One 240-pin SODIMM up to 8GB, DDR3L 1066/1333 /1600 MHz

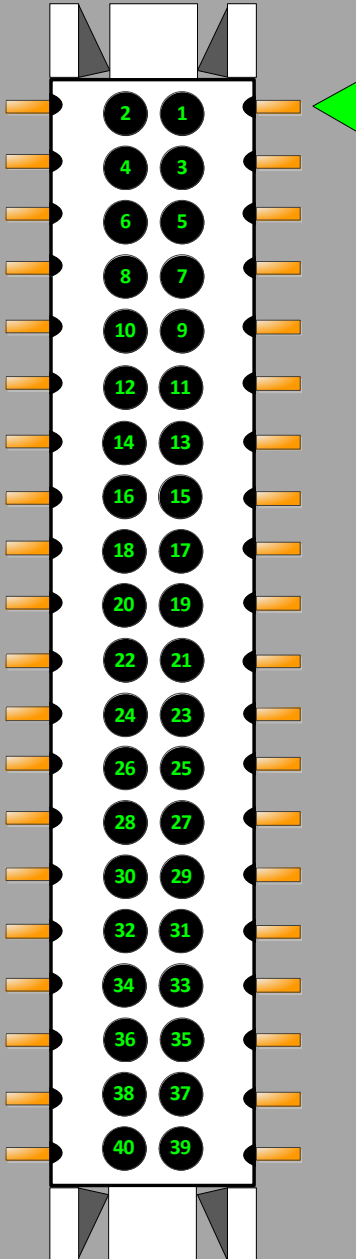
Application Industry

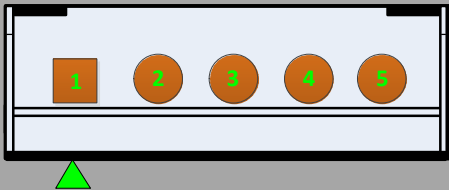
Medical industry, Traffic industry, Advertising Machine, Financial industry,

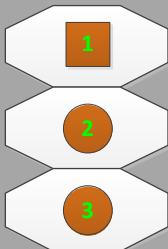
Specifications

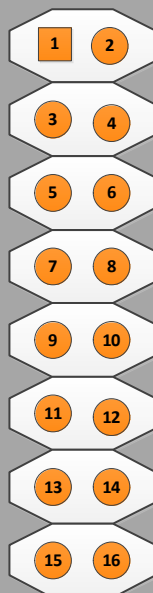
CPU	CPU	Intel Baytrail J1900 2 GHz- 2.4 GHz
	Instruction Set	64bit
	Lithography	22nm
Memory	Type	SODIMM DDR3L No ECC、Support 1333/1600/1867 MHz
	Channel	Single
	Max Memory Size	8GB
Graphics	Graphics	Intel HD Graphics
	Displays Supported	3 Display Port (VGA + DVI + EDP) or (VGA + JVGA + LVDS)
Audio	ALC662 HD Audio	x1 Audio line out, x1 Audio line in, x1 MIC, 4ohm & 2W Power amplifier
Ethernet	RTL8111F	x2 10/100/1000M BASET LAN
Internal Connector	MSATA	x1 MINIPCIIE
	SATA	x1 7PIN SATA
	WIFI/4G	x1 Half length MINIPCIIE, x1 Standard Sim Socket
	VGA	x1 2x5 DuPont Header (Optional)
	LVDS	x1 2x20 Header Box (Optional)
	EDP	x1 2x8 DuPont Header (Optional)
	PCIE1X	x1 PCIE1X
	COM	x8 2x5 Header Box, Powered 5V (Optional)
	USB	x4 2X5 DuPont Header (support 2*port USB2.0)
	PS2	x1 2x4 DuPont Header
	GPIO	x1 2X6 DuPont Header, 8*independent Programming GPIO
I/O	Input DC connector	x2 1XDC jack, 1XATX Power 4Pin connector
	LED	x1 Power Red LED and HDD Green LED
	COM	x2 DB9 Powered 5V/12V (Optional)
	USB	x4 1*Ports USB3.0 and 3*USB2.0
	RJ45	x2 Single RJ45
	Audio	x1 Support Line out, Line in, mic
	VGA	x1 Standard VGA
	DVI	x1 Standard DVI
BIOS	Vender	AMI
	ACPI	Supported
Power	POWER Brick	9V-24V DC POWER Brick
Dimensions	PCB	170*170MM
Requirements Environment	Operation	-15℃~+50℃, 10%~95% RTH
	Storage	-40℃~+70℃, 10%~95% RTH

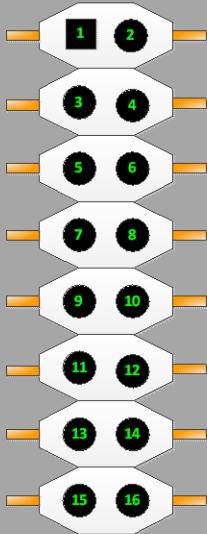


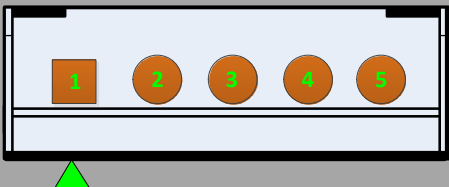
LVDS	LVDS																																																																																
PIN Define																																																																																	
	<table><tr><td>1</td><td>LCD_VDD</td><td>2</td><td>LCD_VDD</td></tr><tr><td>3</td><td>LCD_VDD</td><td>4</td><td>LCD_VDD</td></tr><tr><td>5</td><td>BKLT_ON</td><td>6</td><td>BKLT_CTL</td></tr><tr><td>7</td><td>GND</td><td>8</td><td>GND</td></tr><tr><td>9</td><td>LVDS_D1+</td><td>10</td><td>LVDS_D0+</td></tr><tr><td>11</td><td>LVDS_D1-</td><td>12</td><td>LVDS_D0-</td></tr><tr><td>13</td><td>GND</td><td>14</td><td>GND</td></tr><tr><td>15</td><td>LVDS_D3+</td><td>16</td><td>LVDS_D2+</td></tr><tr><td>17</td><td>LVDS_D3-</td><td>18</td><td>LVDS_D2-</td></tr><tr><td>19</td><td>GND</td><td>20</td><td>GND</td></tr><tr><td>21</td><td>LVDS_D5+</td><td>22</td><td>LVDS_D4+</td></tr><tr><td>23</td><td>LVDS_D5-</td><td>24</td><td>LVDS_D4-</td></tr><tr><td>25</td><td>GND</td><td>26</td><td>GND</td></tr><tr><td>27</td><td>LVDS_D7+</td><td>28</td><td>LVDS_D6+</td></tr><tr><td>29</td><td>LVDS_D7-</td><td>30</td><td>LVDS_D6-</td></tr><tr><td>31</td><td>GND</td><td>32</td><td>GND</td></tr><tr><td>33</td><td>LVDS_CLK2+</td><td>34</td><td>LVDS_CLK1+</td></tr><tr><td>35</td><td>LVDS_CLK2-</td><td>36</td><td>LVDS_CLK1-</td></tr><tr><td>37</td><td>GND</td><td>38</td><td>GND</td></tr><tr><td>39</td><td>+V12S</td><td>40</td><td>+V12S</td></tr></table>	1	LCD_VDD	2	LCD_VDD	3	LCD_VDD	4	LCD_VDD	5	BKLT_ON	6	BKLT_CTL	7	GND	8	GND	9	LVDS_D1+	10	LVDS_D0+	11	LVDS_D1-	12	LVDS_D0-	13	GND	14	GND	15	LVDS_D3+	16	LVDS_D2+	17	LVDS_D3-	18	LVDS_D2-	19	GND	20	GND	21	LVDS_D5+	22	LVDS_D4+	23	LVDS_D5-	24	LVDS_D4-	25	GND	26	GND	27	LVDS_D7+	28	LVDS_D6+	29	LVDS_D7-	30	LVDS_D6-	31	GND	32	GND	33	LVDS_CLK2+	34	LVDS_CLK1+	35	LVDS_CLK2-	36	LVDS_CLK1-	37	GND	38	GND	39	+V12S	40	+V12S
	1	LCD_VDD	2	LCD_VDD																																																																													
	3	LCD_VDD	4	LCD_VDD																																																																													
	5	BKLT_ON	6	BKLT_CTL																																																																													
	7	GND	8	GND																																																																													
	9	LVDS_D1+	10	LVDS_D0+																																																																													
	11	LVDS_D1-	12	LVDS_D0-																																																																													
	13	GND	14	GND																																																																													
	15	LVDS_D3+	16	LVDS_D2+																																																																													
	17	LVDS_D3-	18	LVDS_D2-																																																																													
	19	GND	20	GND																																																																													
	21	LVDS_D5+	22	LVDS_D4+																																																																													
	23	LVDS_D5-	24	LVDS_D4-																																																																													
	25	GND	26	GND																																																																													
27	LVDS_D7+	28	LVDS_D6+																																																																														
29	LVDS_D7-	30	LVDS_D6-																																																																														
31	GND	32	GND																																																																														
33	LVDS_CLK2+	34	LVDS_CLK1+																																																																														
35	LVDS_CLK2-	36	LVDS_CLK1-																																																																														
37	GND	38	GND																																																																														
39	+V12S	40	+V12S																																																																														
Type	2x20 PH=0.15mm																																																																																

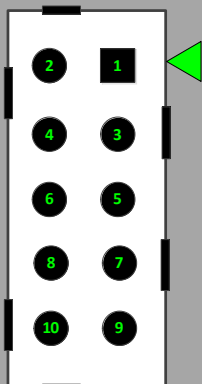
VP1	LVDS Power and Control										
PIN Define	 <table border="1"> <tr><td>1</td><td>+V12S</td></tr> <tr><td>2</td><td>GND</td></tr> <tr><td>3</td><td>LVDS_BKLT_ON</td></tr> <tr><td>4</td><td>LVDS_BKLT_PWM</td></tr> <tr><td>5</td><td>+V5S</td></tr> </table>	1	+V12S	2	GND	3	LVDS_BKLT_ON	4	LVDS_BKLT_PWM	5	+V5S
1	+V12S										
2	GND										
3	LVDS_BKLT_ON										
4	LVDS_BKLT_PWM										
5	+V5S										
Type	1X5 Header Box, PH=2.54mm										

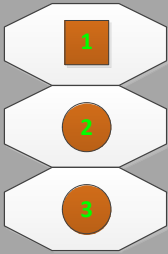
JLV	LVDS Power option						
PIN Define	 <table border="1"> <tr> <td>1</td><td>+V3.3S</td></tr> <tr> <td>2</td><td>LCD_VDD</td></tr> <tr> <td>3</td><td>+V5S</td></tr> </table>	1	+V3.3S	2	LCD_VDD	3	+V5S
1	+V3.3S						
2	LCD_VDD						
3	+V5S						
Type	1X3 DuPont Header, PH=2.54mm						
Memo	Default Setting: JLV (1-2)						

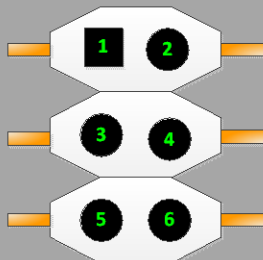
JVGA2	VGA header Pin define																																
PIN Define	 <table><tr><td>1</td><td>RED</td><td>2</td><td>GND</td></tr><tr><td>3</td><td>VCC</td><td>4</td><td>GREEN</td></tr><tr><td>5</td><td>NC</td><td>6</td><td>DDCDATA</td></tr><tr><td>7</td><td>BLUE</td><td>8</td><td>GND</td></tr><tr><td>9</td><td>HSYNC</td><td>10</td><td>VCC</td></tr><tr><td>11</td><td>VCC</td><td>12</td><td>VSYNC</td></tr><tr><td>13</td><td>GND</td><td>14</td><td>GND</td></tr><tr><td>15</td><td>DDCCLK</td><td>16</td><td>NC</td></tr></table>	1	RED	2	GND	3	VCC	4	GREEN	5	NC	6	DDCDATA	7	BLUE	8	GND	9	HSYNC	10	VCC	11	VCC	12	VSYNC	13	GND	14	GND	15	DDCCLK	16	NC
1	RED	2	GND																														
3	VCC	4	GREEN																														
5	NC	6	DDCDATA																														
7	BLUE	8	GND																														
9	HSYNC	10	VCC																														
11	VCC	12	VSYNC																														
13	GND	14	GND																														
15	DDCCLK	16	NC																														
Type	2x8 DuPont Header, PH=2.54mm																																
Memo	VCC=5V optional																																

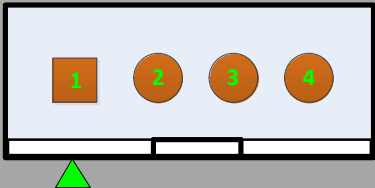
EDP	EDP Pin Define																																			
PIN Define			<table><tr><td>1</td><td>VCC (3.3V)</td><td>2</td><td>VCC (3.3V)</td></tr><tr><td>3</td><td>GND</td><td>4</td><td>GND</td></tr><tr><td>5</td><td>EDP_TX0+</td><td>6</td><td>EDP_TX1+</td></tr><tr><td>7</td><td>EDP_TX0-</td><td>8</td><td>EDP_TX1-</td></tr><tr><td>9</td><td>GND</td><td>10</td><td>GND</td></tr><tr><td>11</td><td>EDP_AUX+</td><td>12</td><td>EDP_HPDP</td></tr><tr><td>13</td><td>EDP_AUX-</td><td>14</td><td>GND</td></tr><tr><td>15</td><td>NC</td><td>16</td><td>GND</td></tr></table>		1	VCC (3.3V)	2	VCC (3.3V)	3	GND	4	GND	5	EDP_TX0+	6	EDP_TX1+	7	EDP_TX0-	8	EDP_TX1-	9	GND	10	GND	11	EDP_AUX+	12	EDP_HPDP	13	EDP_AUX-	14	GND	15	NC	16	GND
	1	VCC (3.3V)	2	VCC (3.3V)																																
	3	GND	4	GND																																
	5	EDP_TX0+	6	EDP_TX1+																																
	7	EDP_TX0-	8	EDP_TX1-																																
	9	GND	10	GND																																
	11	EDP_AUX+	12	EDP_HPDP																																
	13	EDP_AUX-	14	GND																																
15	NC	16	GND																																	
Type	2x8 DuPont Header, PH=2.54mm																																			

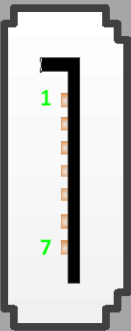
ICVN_EDP	EDP Power and control										
PIN Define	 <table border="1"> <tr> <td>1</td><td>+V12S</td></tr> <tr> <td>2</td><td>GND</td></tr> <tr> <td>3</td><td>EDP_BKLT_ON</td></tr> <tr> <td>4</td><td>EDP_BKLT_PWM</td></tr> <tr> <td>5</td><td>+V5S</td></tr> </table>	1	+V12S	2	GND	3	EDP_BKLT_ON	4	EDP_BKLT_PWM	5	+V5S
1	+V12S										
2	GND										
3	EDP_BKLT_ON										
4	EDP_BKLT_PWM										
5	+V5S										
Type	1X5 Header Box, PH=2.54mm										

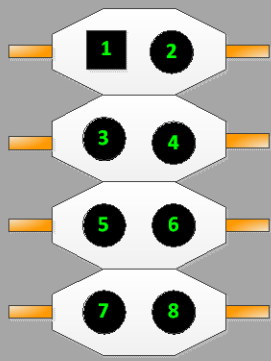
COM	COM3-COM10																				
PIN Define	 <table><tr><td>1</td><td>DCD</td><td>2</td><td>RXD</td></tr><tr><td>3</td><td>TXD</td><td>4</td><td>DTR</td></tr><tr><td>5</td><td>GND</td><td>6</td><td>DSR</td></tr><tr><td>7</td><td>RTS</td><td>8</td><td>CTS</td></tr><tr><td>9</td><td>RI_MUX</td><td>10</td><td>NC</td></tr></table>	1	DCD	2	RXD	3	TXD	4	DTR	5	GND	6	DSR	7	RTS	8	CTS	9	RI_MUX	10	NC
1	DCD	2	RXD																		
3	TXD	4	DTR																		
5	GND	6	DSR																		
7	RTS	8	CTS																		
9	RI_MUX	10	NC																		
Type	2X5 Header Box PH=2.0 mm																				

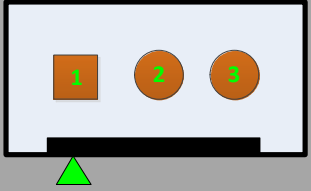
COM Power	JC3-JC10										
PIN Define	 <table border="1"> <tr><td>1</td><td>RI</td></tr> <tr><td>2</td><td>RI_MUX</td></tr> <tr><td>3</td><td>5V</td></tr> <tr><td>1-2</td><td>RI</td></tr> <tr><td>3-4</td><td>5V</td></tr> </table>	1	RI	2	RI_MUX	3	5V	1-2	RI	3-4	5V
1	RI										
2	RI_MUX										
3	5V										
1-2	RI										
3-4	5V										
Type	1X3 DuPont Header, PH=2.0mm										
Memo	Default Setting: JC3-JC10(1-2)										

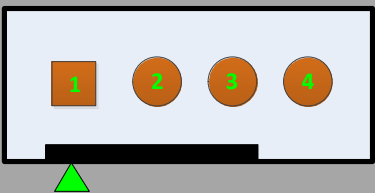
COM Power	JC1/JC2										
PIN Define	 <table><tr><td>1</td><td>5V</td><td>2</td><td rowspan="3">RI_MUX</td></tr><tr><td>3</td><td>RI</td><td>4</td></tr><tr><td>5</td><td>12V</td><td>6</td></tr></table>	1	5V	2	RI_MUX	3	RI	4	5	12V	6
1	5V	2	RI_MUX								
3	RI	4									
5	12V	6									
Type	2x3 DuPont Header, PH=2.0mm										
Memo	FOR DB9 (Pin9) com power optioan, Default Setting: JC1(3-4) JC2(3-4)										

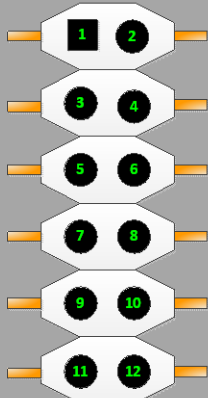
SATAPWR1	SATA POWER								
PIN Define	 <table border="1"> <tr><td>1</td><td>+V12S</td></tr> <tr><td>2</td><td>GND</td></tr> <tr><td>3</td><td>GND</td></tr> <tr><td>4</td><td>+V5S</td></tr> </table>	1	+V12S	2	GND	3	GND	4	+V5S
1	+V12S								
2	GND								
3	GND								
4	+V5S								
Type	1X4 DuPont Header, PH=2.54mm								

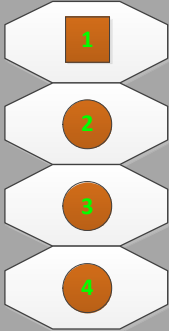
SATA	SATA1														
PIN Define	 <table border="1"> <tr><td>1</td><td>GND</td></tr> <tr><td>2</td><td>TX+</td></tr> <tr><td>3</td><td>TX-</td></tr> <tr><td>4</td><td>GND</td></tr> <tr><td>5</td><td>RX-</td></tr> <tr><td>6</td><td>RX+</td></tr> <tr><td>7</td><td>GND</td></tr> </table>	1	GND	2	TX+	3	TX-	4	GND	5	RX-	6	RX+	7	GND
1	GND														
2	TX+														
3	TX-														
4	GND														
5	RX-														
6	RX+														
7	GND														
Type	7PIN Standard SATA														

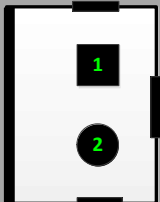
J_PS2	Ps2 header																
PIN Define	 <table><tr><td>1</td><td>KB_DATA</td><td>2</td><td>MS_DATA</td></tr><tr><td>3</td><td>KB_CLK</td><td>4</td><td>MS_CLK</td></tr><tr><td>5</td><td>GND</td><td>6</td><td>GND</td></tr><tr><td>7</td><td>+V5S</td><td>8</td><td>+V5S</td></tr></table>	1	KB_DATA	2	MS_DATA	3	KB_CLK	4	MS_CLK	5	GND	6	GND	7	+V5S	8	+V5S
1	KB_DATA	2	MS_DATA														
3	KB_CLK	4	MS_CLK														
5	GND	6	GND														
7	+V5S	8	+V5S														
Type	2x4 DuPont Header, PH=2.0mm																

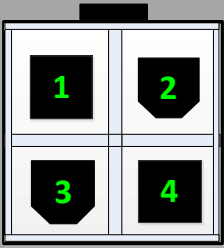
SYS_FAN	System Fan Power and sense						
PIN Define	 <table border="1"> <tr> <td>1</td><td>GND</td></tr> <tr> <td>2</td><td>+V12S</td></tr> <tr> <td>3</td><td>FAN_TAC</td></tr> </table>	1	GND	2	+V12S	3	FAN_TAC
1	GND						
2	+V12S						
3	FAN_TAC						
Type	1X3 DuPont Header, PH=2.54mm						

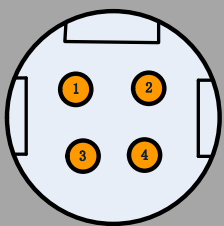
CPU_FAN	CPU FAN Power and Control								
PIN Define	 <table border="1"> <tr> <td>1</td><td>GND</td></tr> <tr> <td>2</td><td>+V12S</td></tr> <tr> <td>3</td><td>FAN_TAC</td></tr> <tr> <td>4</td><td>FAN_CTL</td></tr> </table>	1	GND	2	+V12S	3	FAN_TAC	4	FAN_CTL
1	GND								
2	+V12S								
3	FAN_TAC								
4	FAN_CTL								
Type	1X4 DuPont Header, PH=2.54mm								

GPI0	GPI0 Pin Define																											
PIN Define			<table><tr><td>1</td><td>+V5S</td><td>2</td><td>+V12S</td></tr><tr><td>3</td><td>GPI01</td><td>4</td><td>GPI05</td></tr><tr><td>5</td><td>GPI02</td><td>6</td><td>GPI06</td></tr><tr><td>7</td><td>GPI03</td><td>8</td><td>GPI07</td></tr><tr><td>9</td><td>GPI04</td><td>10</td><td>GPI08</td></tr><tr><td>11</td><td>GND</td><td>12</td><td>GND</td></tr></table>		1	+V5S	2	+V12S	3	GPI01	4	GPI05	5	GPI02	6	GPI06	7	GPI03	8	GPI07	9	GPI04	10	GPI08	11	GND	12	GND
	1	+V5S	2	+V12S																								
	3	GPI01	4	GPI05																								
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	9	GPI04	10	GPI08																								
	11	GND	12	GND																								
Type	2x6 DuPont Header, PH=2.54mm																											

JCOMS	Cmos and ME fucktion header						
PIN Define	 <table border="1"> <tr> <td>1-2</td><td>Normal</td></tr> <tr> <td>2-3</td><td>Clear cmos</td></tr> <tr> <td>3-4</td><td>Override ME</td></tr> </table>	1-2	Normal	2-3	Clear cmos	3-4	Override ME
1-2	Normal						
2-3	Clear cmos						
3-4	Override ME						
Type	1X4 DuPont Header, PH=2.0mm						
Memo	Default Setting: JCmos(1-2)						

SPKR_L/SPKR_R	Power amplifier				
PIN Define	 <table border="1"> <tr> <td>1</td><td>SPKR_L-/SPKR_R-</td></tr> <tr> <td>2</td><td>SPKR_L+/SPKR_R+</td></tr> </table>	1	SPKR_L-/SPKR_R-	2	SPKR_L+/SPKR_R+
1	SPKR_L-/SPKR_R-				
2	SPKR_L+/SPKR_R+				
Type	1x2 Wafer Box PH=2.0 mm				

DC-PWR	ATX 4Pin				
PIN Define	 <table border="1"> <tr> <td>1-2</td><td>DC+</td></tr> <tr> <td>3-4</td><td>DC-</td></tr> </table>	1-2	DC+	3-4	DC-
1-2	DC+				
3-4	DC-				
Type	ATX Standard 4 pin connector				

DC-PWR	DC-JACK				
PIN Define	 <table border="1"> <tr> <td>1-2</td><td>DC+</td></tr> <tr> <td>3-4</td><td>DC-</td></tr> </table>	1-2	DC+	3-4	DC-
1-2	DC+				
3-4	DC-				
Type	Standard DC Jack				