

Product Spec

Motherboard

RA-C1001 V1.0

Prepared by : wangwei

Date: 2017/07/30

Approved by: PMZ

Date: 2017/07/30

Revision History			
Revision	Date	Description	Author
V1.0	2017/07/30	First release	Wangwei



Specifications

Features

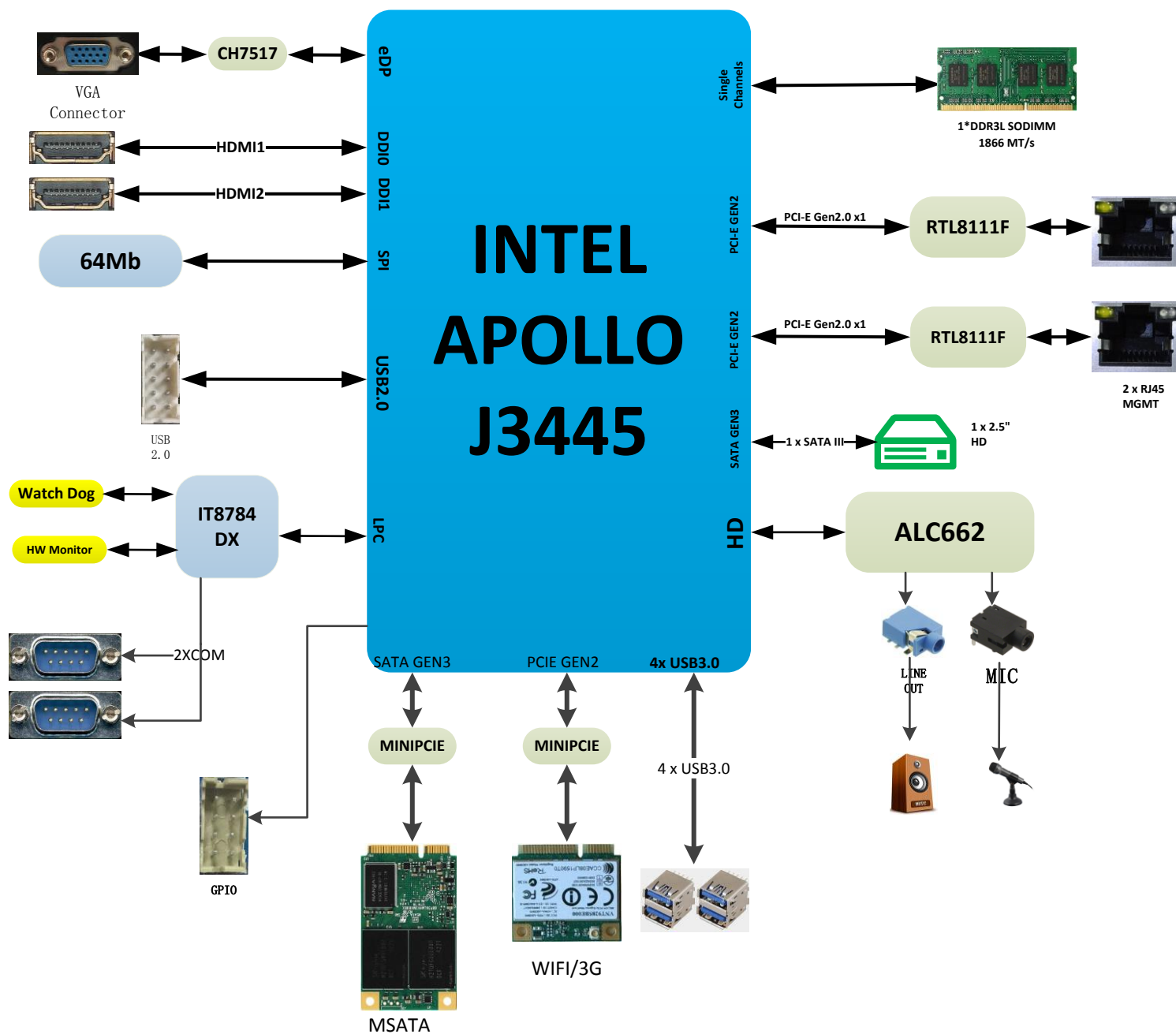
- *Intel Apollo Platform, J3455 CPU, 1.5GHz, 4*Cores,4*Threads
- *Supports 3 independent Display, support android
- *Supports 2 X 10/100/1000M LAN adapter
- *Support 2 X USB2.0, 4 X USB3.0
- *Support 2 X Powered COMs, 1 X SATAIII, 1 X MSATA , 1 X WIFI/4G LTE
- *Support HD Audio , 1 X Audio out, 1X MIC
- *Support 12DC Adapter
- *Support Eup Power saving
- *One 240-pin SODIMM up to 8GB, DDR3L 1066/1333 /1600 MHz

Application Industry

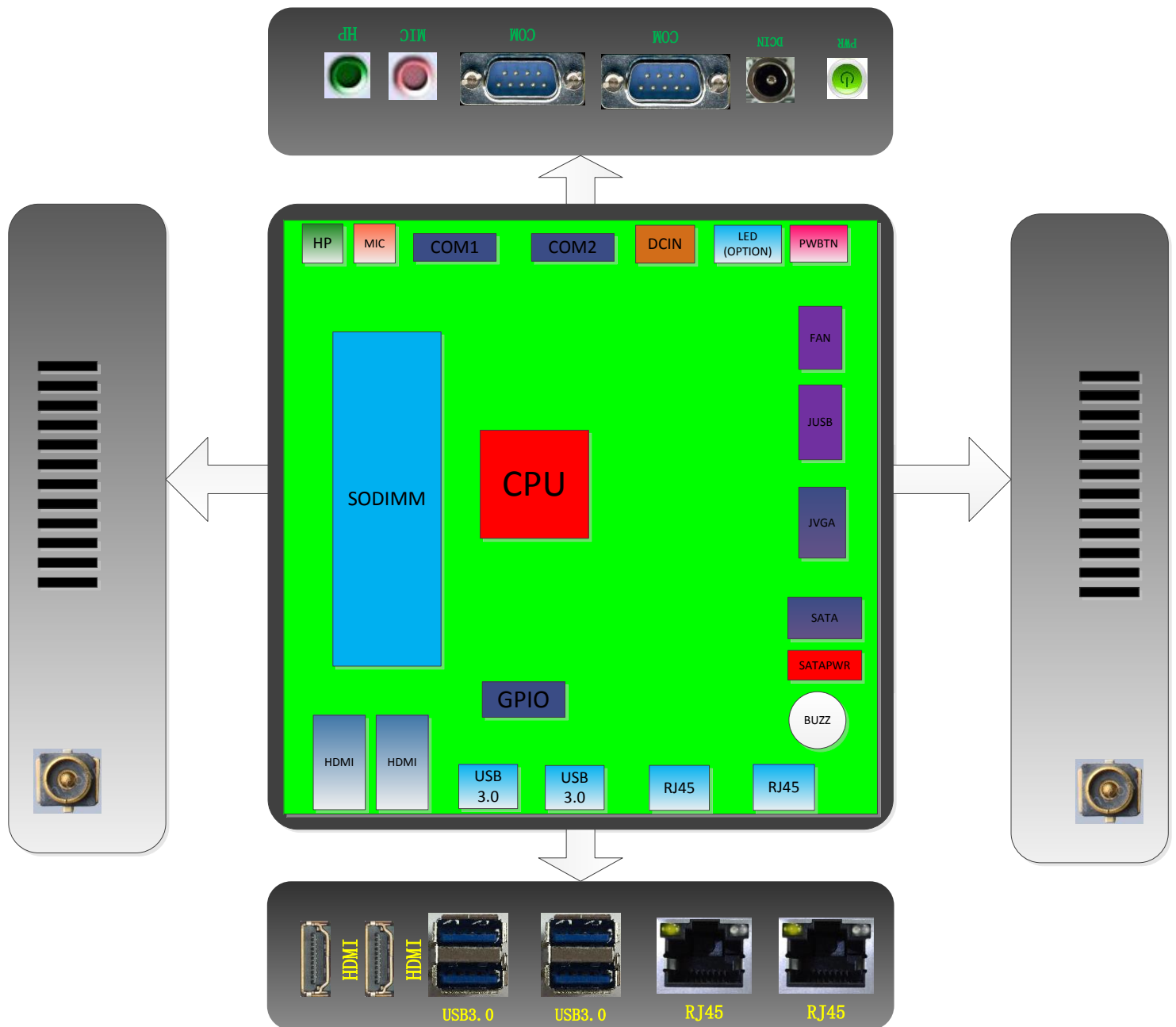
Medical industry.Traffic industry, Advertising Machine,Financial industry,

CPU	CPU	Intel Apollo J3455 of 4 Cores 4 Threads
	Instruction Set	64bit
	Lithography	14nm
	Frequency	1.5 GHz– 2.3 GHz
Memory	Type	SODIMM DDR3L No ECC , 1333/1600/1867 MHz
	Channel	1C1P
	Max Memory Size	8GB
Graphics	Graphics	Intel HD Graphics
	Graphics Frequency	250 MHz – 750MHz
	Displays Supported	3 Independent Display (2 x HDMI, 1 x VGA)
Audio	ALC662 HD Audio	x1 Audio line out, x1 MIC
Ethernet	RTL8111F	x2 10/100/1000M BASET LAN
Internal Connector	SIM	x1 Standard Sim Socket
	MSATA	x1 MINIPCIE
	SATA	x1 7PIN SATA
	WIFI/4G	x1 Half length MINIPCIE
	VGA	x1 2x5 Header Box (Optional)
	COM	x2 COM1–COM2 RS232 DB9 Connector, Powered 5V/12V
	USB	x2 2 port USB2.0, 2X5 Header Box
	GPIO	x1 2X4 Header , 4 independent Programing GPIO
I/O	PWRBTN	x1 PWRBTN With Green LED and Power Symbol
	DC JACK	x1 12V
	LED	x1 Green LED WIFI/HDD Indecater (Optional)
	COM	x2 DB9
	USB	x4 4 Ports USB3.0 (Two Dual USB3.0 Connector)
	RJ45	x2 Single RJ45
	MIC	x1 Single Pink 3.5mm Audio Jack
	HP	x1 Single Green 3.5mm Audio Jack
	HDMI	x2 Standard HDMI Connector
BIOS	Vender	AMI
	ACPI	Supported
	EUP	Supported
Power	POWER Brick	12V/3A DC POWER Brick
Dimensions	CASE/PCB	137*127*41mm/121*121mm
Requirements Environment	Operation	–15℃~+50℃, 10%~95% RTH
	Storage	–40℃~+70℃, 10%~95% RTH

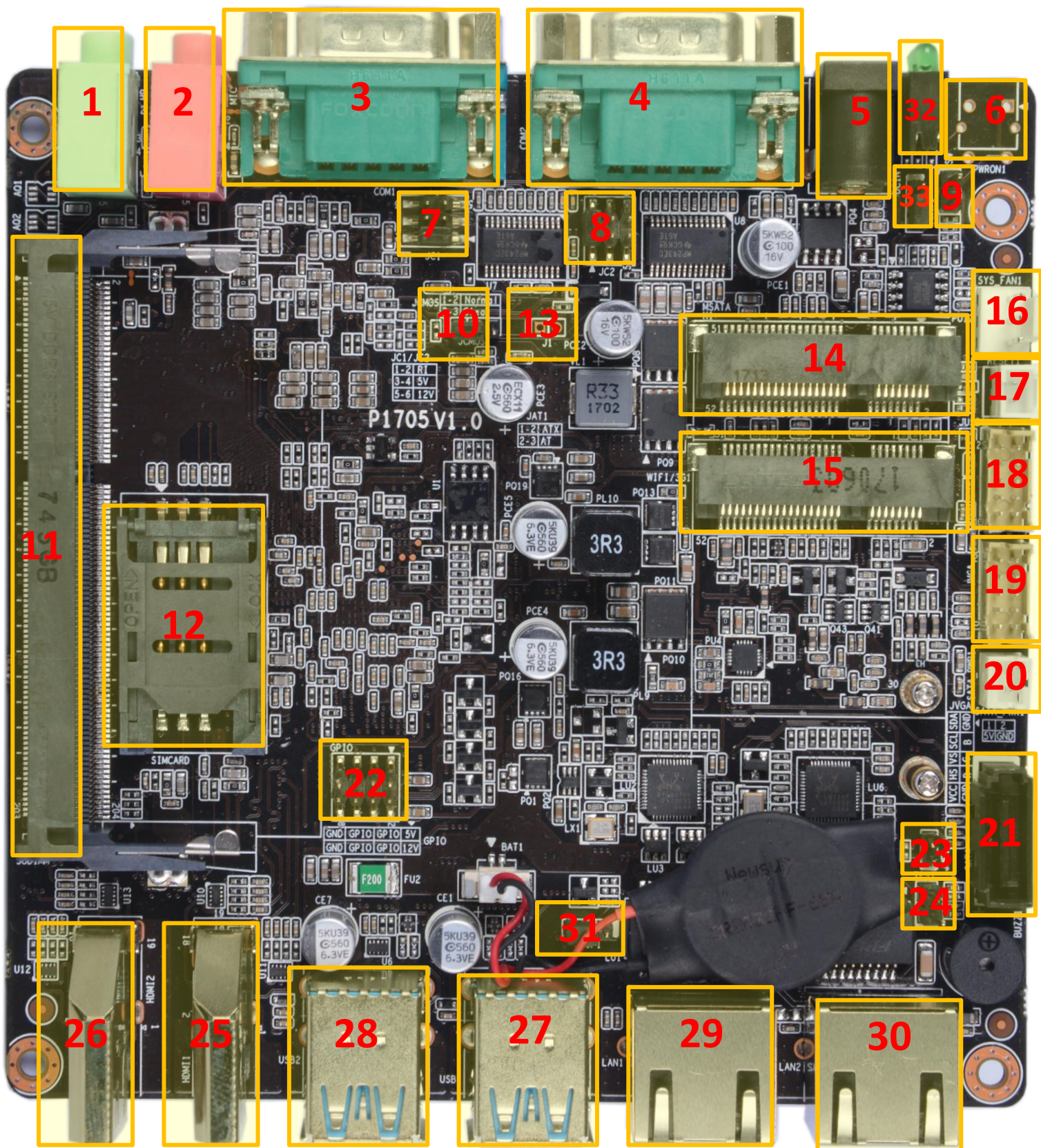
RA-C1001 - Main Board Block Diagram

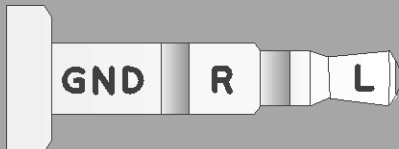


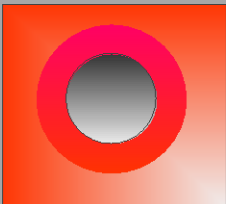
RA-C1001- System diagram

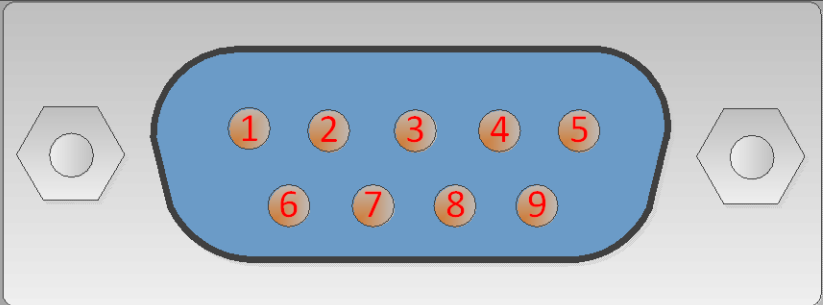




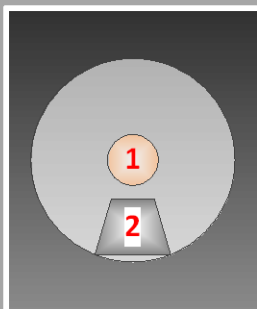


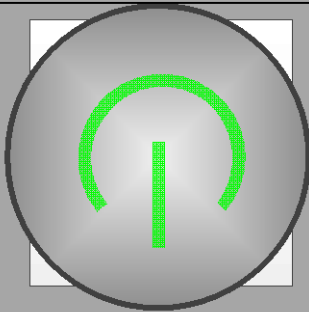
Audio	HP (①)						
PIN Define	  <table border="1"> <tr> <td>L</td><td>Audio L Channel</td></tr> <tr> <td>R</td><td>Audio R Channel</td></tr> <tr> <td>GND</td><td>GND</td></tr> </table>	L	Audio L Channel	R	Audio R Channel	GND	GND
L	Audio L Channel						
R	Audio R Channel						
GND	GND						
Type	Standard Single 3.5mm Audio jack						
Memo	Realtek ALC662 Audio codec on board (Line - out Port and on the rear I/O)						

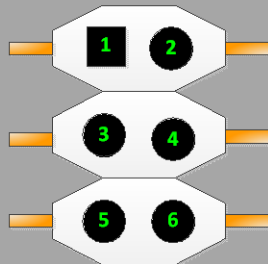
Audio	MIC (②)						
PIN Define	  <table border="1"> <tr> <td>L</td><td>MIC L Channel</td></tr> <tr> <td>R</td><td>MIC R Channel</td></tr> <tr> <td>GND</td><td>GND</td></tr> </table>	L	MIC L Channel	R	MIC R Channel	GND	GND
L	MIC L Channel						
R	MIC R Channel						
GND	GND						
Type	Standard Single 3.5mm Audio jack						
Memo	Realtek ALC662 Audio codec on board (Mic IN Port and on the rear I/O)						

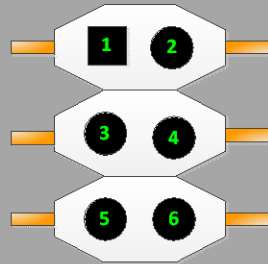
COM	COM1 (③)			
PIN Define				
	1	DCD	2	RXD
	3	TXD	4	DTR
	5	GND	6	DSR
	7	RTS	8	CTS
	9	5V/12V/RI		
Type	Standard DB9 COM Connector			
Memo	1. Support RS232 2. Powered 5V/12V, Setting by jumper, refer to JC1			

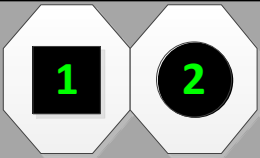
COM	COM2 (④)			
PIN Define				
	1	DCD	2	RXD
	3	TXD	4	DTR
	5	GND	6	DSR
	7	RTS	8	CTS
	9	5V/12V/RI		
Type	Standard DB9 COM Connector			
Memo	1. Support RS232 2. Powered 5V/12V,Setting by jumper, refer to jc2			

DCIN	DCIN1 (⑤)						
PIN Define		<table><tr><td>1</td><td>+12V</td></tr><tr><td>2</td><td>GND</td></tr></table>		1	+12V	2	GND
1	+12V						
2	GND						
Type	DC JACK, PIN C=2.5mm						

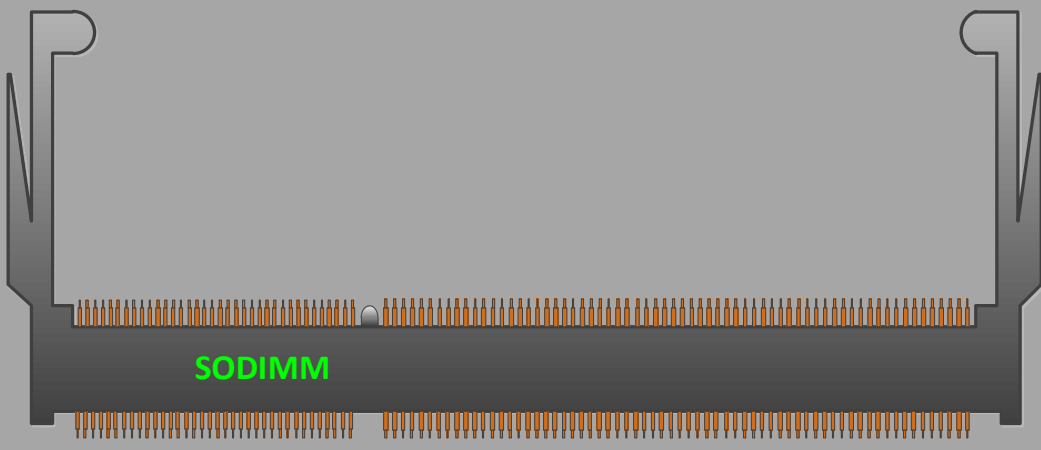
PWRBTN	PWRON1 (⑥)						
PIN Define		<table><tr><td>1</td><td>PWRBTN+</td></tr><tr><td>2</td><td>GND</td></tr></table>		1	PWRBTN+	2	GND
1	PWRBTN+						
2	GND						
Type	POWER BTN With White LED						

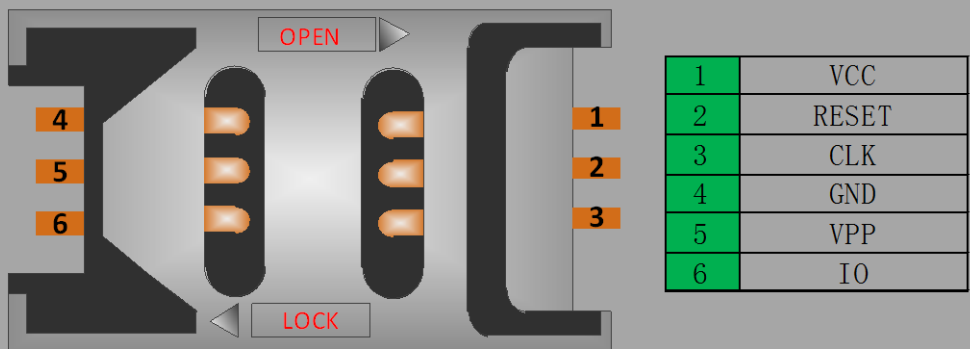
JC1	JC1 (⑦)												
PIN Define	 <table><tr><td>1</td><td>RI</td><td>2</td><td>COM1. PIN9</td></tr><tr><td>3</td><td>5V</td><td>4</td><td>COM1. PIN9</td></tr><tr><td>5</td><td>12V</td><td>6</td><td>COM1. PIN9</td></tr></table>	1	RI	2	COM1. PIN9	3	5V	4	COM1. PIN9	5	12V	6	COM1. PIN9
1	RI	2	COM1. PIN9										
3	5V	4	COM1. PIN9										
5	12V	6	COM1. PIN9										
Type	2x3 DuPont Header , PH=2.0mm, Refer to COM1												
Memo	Default setting:1-2												

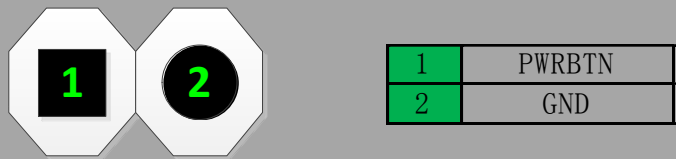
JC1	JC2 (⑧)												
PIN Define	 <table><tr><td>1</td><td>RI</td><td>2</td><td>COM2. PIN9</td></tr><tr><td>3</td><td>5V</td><td>4</td><td>COM2. PIN9</td></tr><tr><td>5</td><td>12V</td><td>6</td><td>COM2. PIN9</td></tr></table>	1	RI	2	COM2. PIN9	3	5V	4	COM2. PIN9	5	12V	6	COM2. PIN9
1	RI	2	COM2. PIN9										
3	5V	4	COM2. PIN9										
5	12V	6	COM2. PIN9										
Type	2x3 DuPont Header, PH=2.0mm, Refer to COM2												
Memo	Default setting:1-2												

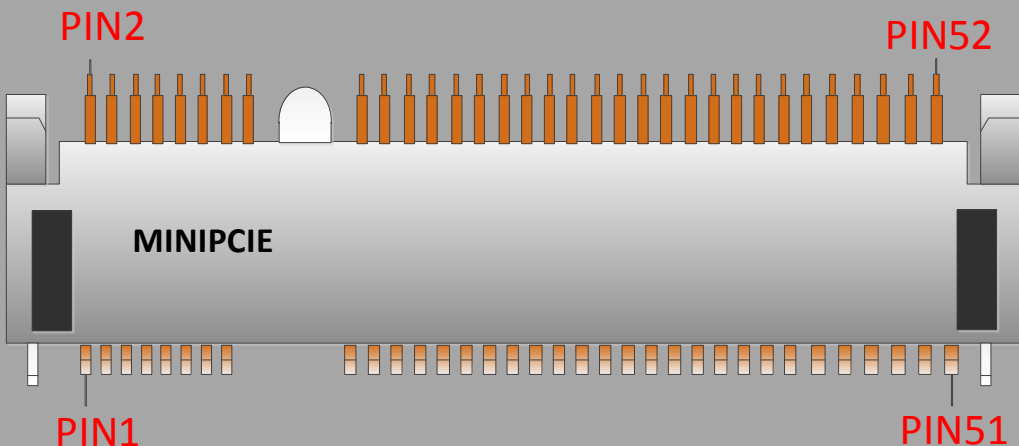
SW	SW (⑨)				
PIN Define	 <table border="1"> <tr> <td>1</td><td>PWRBTN</td></tr> <tr> <td>2</td><td>GND</td></tr> </table>	1	PWRBTN	2	GND
1	PWRBTN				
2	GND				
Type	1x2 DuPont Header PH=2.54mm				
Memo	Internal Debug only				

JCMOS	JCMOS1 (⑩)									
PIN Define	 <table><tr><th>Pin</th><th>1-2</th><th>2-3</th></tr><tr><td>Define</td><td>Normal</td><td>Clear</td></tr><tr><td>Default</td><td colspan="2">1-2</td></tr></table>	Pin	1-2	2-3	Define	Normal	Clear	Default	1-2	
Pin	1-2	2-3								
Define	Normal	Clear								
Default	1-2									
Type	1x3 DuPont Header PH=2.0mm									
Memo	Short 2-3, Clear COMS									

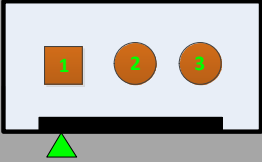
SODIMM	SODIMM (11)
PIN Define	
Type	204PIN Standard SODIMM Socket
Memo	Standard PIN Define, Support DDR3L, Detail refer to JEDEC Specification

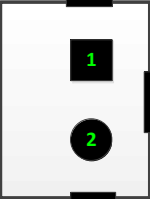
SIM	SIM (12)												
PIN Define	 <table border="1"> <tr><td>1</td><td>VCC</td></tr> <tr><td>2</td><td>RESET</td></tr> <tr><td>3</td><td>CLK</td></tr> <tr><td>4</td><td>GND</td></tr> <tr><td>5</td><td>VPP</td></tr> <tr><td>6</td><td>IO</td></tr> </table>	1	VCC	2	RESET	3	CLK	4	GND	5	VPP	6	IO
1	VCC												
2	RESET												
3	CLK												
4	GND												
5	VPP												
6	IO												
Type	Standard SIM Socket												
Memo	Support 3G												

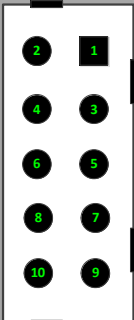
J1	J1 (13)				
PIN Define	 <table border="1"> <tr><td>1</td><td>PWRBTN</td></tr> <tr><td>2</td><td>GND</td></tr> </table>	1	PWRBTN	2	GND
1	PWRBTN				
2	GND				
Type	1x2 DuPont Header PH=2. 54mm				
Memo	Internal Debug only				

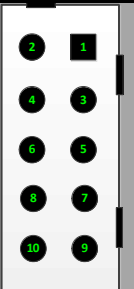
MIMIPCIE	MSATA(14)																																																																																																								
PIN Define																																																																																																									
	<table><tr><td>1</td><td>WAKE#</td><td>2</td><td>+3.3V</td></tr><tr><td>3</td><td>CHANNEL_CLK</td><td>4</td><td>GND</td></tr><tr><td>5</td><td>CHANNEL_DATA</td><td>6</td><td>+1.5V</td></tr><tr><td>7</td><td>CLKREQ#</td><td>8</td><td>FRAME#</td></tr><tr><td>9</td><td>GND</td><td>10</td><td>LAD3</td></tr><tr><td>11</td><td>REFCLK-</td><td>12</td><td>LAD2</td></tr><tr><td>13</td><td>REFCLK+</td><td>14</td><td>LAD1</td></tr><tr><td>15</td><td>GND</td><td>16</td><td>LAD0</td></tr><tr><td>17</td><td>PLTRST</td><td>18</td><td>GND</td></tr><tr><td>19</td><td>LPC_CLK</td><td>20</td><td>DISABLE</td></tr><tr><td>21</td><td>GND</td><td>22</td><td>PERST#</td></tr><tr><td>23</td><td>SATA-RXN</td><td>24</td><td>+3.3VAL</td></tr><tr><td>25</td><td>SATA-RXP</td><td>26</td><td>GND</td></tr><tr><td>27</td><td>GND</td><td>28</td><td>+1.5V</td></tr><tr><td>29</td><td>GND</td><td>30</td><td>SMB_CLK</td></tr><tr><td>31</td><td>SATA-TXN</td><td>32</td><td>SMB_DATA</td></tr><tr><td>33</td><td>SATA-TXP</td><td>34</td><td>GND</td></tr><tr><td>35</td><td>GND</td><td>36</td><td>USBD-</td></tr><tr><td>37</td><td>GND</td><td>38</td><td>USBD+</td></tr><tr><td>39</td><td>+3.3V</td><td>40</td><td>GND</td></tr><tr><td>41</td><td>+3.3V</td><td>42</td><td>LED_WSAN#</td></tr><tr><td>43</td><td>GND</td><td>44</td><td>LED_WLAN#</td></tr><tr><td>45</td><td>RSV</td><td>46</td><td>LED_WPAN#</td></tr><tr><td>47</td><td>RSV</td><td>48</td><td>+1.5V</td></tr><tr><td>49</td><td>RSV</td><td>50</td><td>GND</td></tr><tr><td>51</td><td>RSV</td><td>52</td><td>+3.3V</td></tr></table>	1	WAKE#	2	+3.3V	3	CHANNEL_CLK	4	GND	5	CHANNEL_DATA	6	+1.5V	7	CLKREQ#	8	FRAME#	9	GND	10	LAD3	11	REFCLK-	12	LAD2	13	REFCLK+	14	LAD1	15	GND	16	LAD0	17	PLTRST	18	GND	19	LPC_CLK	20	DISABLE	21	GND	22	PERST#	23	SATA-RXN	24	+3.3VAL	25	SATA-RXP	26	GND	27	GND	28	+1.5V	29	GND	30	SMB_CLK	31	SATA-TXN	32	SMB_DATA	33	SATA-TXP	34	GND	35	GND	36	USBD-	37	GND	38	USBD+	39	+3.3V	40	GND	41	+3.3V	42	LED_WSAN#	43	GND	44	LED_WLAN#	45	RSV	46	LED_WPAN#	47	RSV	48	+1.5V	49	RSV	50	GND	51	RSV	52	+3.3V
	1	WAKE#	2	+3.3V																																																																																																					
	3	CHANNEL_CLK	4	GND																																																																																																					
	5	CHANNEL_DATA	6	+1.5V																																																																																																					
	7	CLKREQ#	8	FRAME#																																																																																																					
	9	GND	10	LAD3																																																																																																					
	11	REFCLK-	12	LAD2																																																																																																					
	13	REFCLK+	14	LAD1																																																																																																					
	15	GND	16	LAD0																																																																																																					
17	PLTRST	18	GND																																																																																																						
19	LPC_CLK	20	DISABLE																																																																																																						
21	GND	22	PERST#																																																																																																						
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27	GND	28	+1.5V																																																																																																						
29	GND	30	SMB_CLK																																																																																																						
31	SATA-TXN	32	SMB_DATA																																																																																																						
33	SATA-TXP	34	GND																																																																																																						
35	GND	36	USBD-																																																																																																						
37	GND	38	USBD+																																																																																																						
39	+3.3V	40	GND																																																																																																						
41	+3.3V	42	LED_WSAN#																																																																																																						
43	GND	44	LED_WLAN#																																																																																																						
45	RSV	46	LED_WPAN#																																																																																																						
47	RSV	48	+1.5V																																																																																																						
49	RSV	50	GND																																																																																																						
51	RSV	52	+3.3V																																																																																																						
Type	MINIPCIE Socket, H=6.8mm																																																																																																								
Memo	Full Length , Support SATA and LPC debug interface																																																																																																								

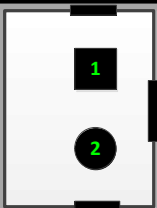
MIMIPCIE	WIFI/3G1 (15)
PIN Define	<

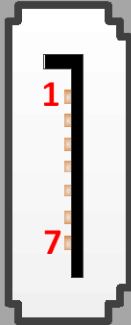
SYS_FAN	SYS_FAN (16)						
PIN Define	 <table border="1" data-bbox="1050 405 1342 524"> <tr> <td>1</td><td>GND</td></tr> <tr> <td>2</td><td>+V12S</td></tr> <tr> <td>3</td><td>SYSFAN_TAC</td></tr> </table>	1	GND	2	+V12S	3	SYSFAN_TAC
1	GND						
2	+V12S						
3	SYSFAN_TAC						
Type	1X3 FAN Connector, PH=2.54mm						

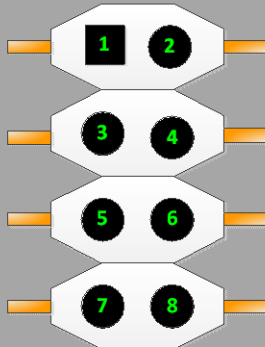
Reset	RESET1 (17)				
PIN Define	 <table border="1" data-bbox="837 828 1046 907"> <tr> <td>1</td><td>FP_RST</td></tr> <tr> <td>2</td><td>GND</td></tr> </table>	1	FP_RST	2	GND
1	FP_RST				
2	GND				
Type	1x2 Wafer Box PH=2.0 mm				
Memo	Short PIN1 and PIN2 Will Reset system				

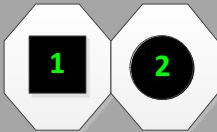
JUSB	JUSB1 (18)																				
PIN Define	 <table><tr><td>1</td><td>GND</td><td>2</td><td>GND</td></tr><tr><td>3</td><td>GND</td><td>4</td><td>GND</td></tr><tr><td>5</td><td>D+</td><td>6</td><td>D+</td></tr><tr><td>7</td><td>D-</td><td>8</td><td>D-</td></tr><tr><td>9</td><td>Vcc</td><td>10</td><td>Vcc</td></tr></table>	1	GND	2	GND	3	GND	4	GND	5	D+	6	D+	7	D-	8	D-	9	Vcc	10	Vcc
1	GND	2	GND																		
3	GND	4	GND																		
5	D+	6	D+																		
7	D-	8	D-																		
9	Vcc	10	Vcc																		
Type	2X5 Header Box PH=2.0 mm																				

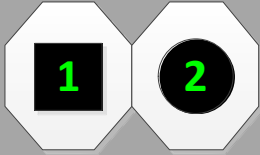
VGA	JVGA1 (19)																							
PIN Define		<table><tr><td>1</td><td>GND</td><td>2</td><td>SDA</td></tr><tr><td>3</td><td>G</td><td>4</td><td>SCK</td></tr><tr><td>5</td><td>B</td><td>6</td><td>VSO</td></tr><tr><td>7</td><td>R</td><td>8</td><td>HS0</td></tr><tr><td>9</td><td>GND</td><td>10</td><td>Vcc</td></tr></table>			1	GND	2	SDA	3	G	4	SCK	5	B	6	VSO	7	R	8	HS0	9	GND	10	Vcc
1	GND	2	SDA																					
3	G	4	SCK																					
5	B	6	VSO																					
7	R	8	HS0																					
9	GND	10	Vcc																					
Type	2X5 Header Box PH=2.0 mm																							

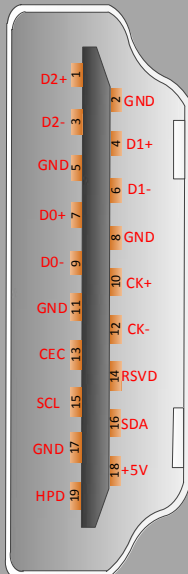
SATA PWR	SATA_PWR1 (20)				
PIN Define	 <table border="1" data-bbox="858 331 1008 412"> <tr> <td>1</td><td>5V</td></tr> <tr> <td>2</td><td>GND</td></tr> </table>	1	5V	2	GND
1	5V				
2	GND				
Type	1x2 Wafer Box PH=2.0 mm				

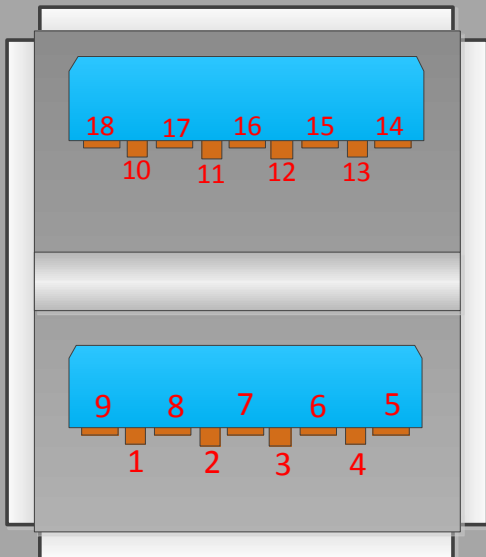
SATA	SATA1 (21)														
PIN Define	 <table border="1" data-bbox="858 721 1008 994"> <tr> <td>1</td><td>GND</td></tr> <tr> <td>2</td><td>TX+</td></tr> <tr> <td>3</td><td>TX-</td></tr> <tr> <td>4</td><td>GND</td></tr> <tr> <td>5</td><td>RX-</td></tr> <tr> <td>6</td><td>RX+</td></tr> <tr> <td>7</td><td>GND</td></tr> </table>	1	GND	2	TX+	3	TX-	4	GND	5	RX-	6	RX+	7	GND
1	GND														
2	TX+														
3	TX-														
4	GND														
5	RX-														
6	RX+														
7	GND														
Type	7PIN SATA														

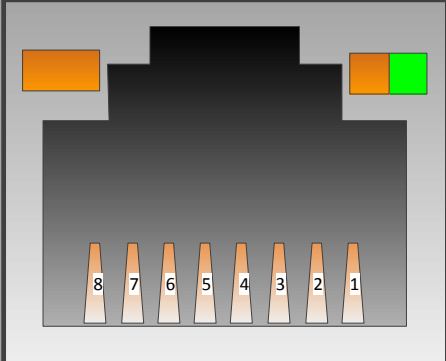
GPIO	GPIO (22)																			
PIN Define	 <table><tr><td>1</td><td>5V</td><td>2</td><td>12V</td></tr><tr><td>3</td><td>GPIO1</td><td>4</td><td>GPIO3</td></tr><tr><td>5</td><td>GPIO2</td><td>6</td><td>GPIO4</td></tr><tr><td>7</td><td>GND</td><td>8</td><td>GND</td></tr></table>				1	5V	2	12V	3	GPIO1	4	GPIO3	5	GPIO2	6	GPIO4	7	GND	8	GND
1	5V	2	12V																	
3	GPIO1	4	GPIO3																	
5	GPIO2	6	GPIO4																	
7	GND	8	GND																	
Type	2x4 DuPont Header, PH=2.0mm																			

TXE	JME1 (23)
PIN Define	
Type	1x2 DuPont Header, PH=2.0mm
Memo	Short PIN1 and PIN2, Disable TXE

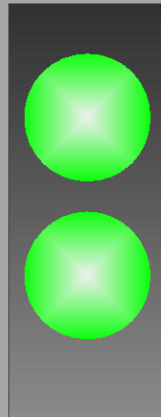
TEST	TEST1(24)				
PIN Define	<table><tr><td>1</td><td>+5VAL</td></tr><tr><td>2</td><td>GND</td></tr></table>	1	+5VAL	2	GND
1	+5VAL				
2	GND				
Type	1x2 DuPont Header PH=2. 2.54mm				
Memo	Internal Debug only				

HDMI	HDMI1(25), HDMI2(26)																																											
PIN Define	<table><tr><td>1</td><td>D2+</td><td>2</td><td>GND</td></tr><tr><td>3</td><td>D2-</td><td>4</td><td>D1+</td></tr><tr><td>5</td><td>GND</td><td>6</td><td>D1-</td></tr><tr><td>7</td><td>D0+</td><td>8</td><td>GND</td></tr><tr><td>9</td><td>D0-</td><td>10</td><td>CK+</td></tr><tr><td>11</td><td>GND</td><td>12</td><td>CK-</td></tr><tr><td>13</td><td>CEC</td><td>14</td><td>RSVD</td></tr><tr><td>15</td><td>SCL</td><td>16</td><td>SDA</td></tr><tr><td>17</td><td>GND</td><td>18</td><td>+5V</td></tr><tr><td>19</td><td>HPD</td><td></td><td></td></tr></table>				1	D2+	2	GND	3	D2-	4	D1+	5	GND	6	D1-	7	D0+	8	GND	9	D0-	10	CK+	11	GND	12	CK-	13	CEC	14	RSVD	15	SCL	16	SDA	17	GND	18	+5V	19	HPD		
1	D2+	2	GND																																									
3	D2-	4	D1+																																									
5	GND	6	D1-																																									
7	D0+	8	GND																																									
9	D0-	10	CK+																																									
11	GND	12	CK-																																									
13	CEC	14	RSVD																																									
15	SCL	16	SDA																																									
17	GND	18	+5V																																									
19	HPD																																											
Type	Standard HDMI connector, Cross stand																																											

USB	USB1 (27), USB2 (28)																																							
PIN Define	 <table><tr><td>1</td><td>Vbus</td><td>2</td><td>D1-</td></tr><tr><td>3</td><td>D1+</td><td>4</td><td>GND</td></tr><tr><td>5</td><td>SSRX1-</td><td>6</td><td>SSRX1+</td></tr><tr><td>7</td><td>GND</td><td>8</td><td>SSTX1-</td></tr><tr><td>9</td><td>SSTX1+</td><td>10</td><td>Vbus</td></tr><tr><td>11</td><td>D2-</td><td>12</td><td>D2+</td></tr><tr><td>13</td><td>GND</td><td>14</td><td>SSRX2-</td></tr><tr><td>15</td><td>SSRX2+</td><td>16</td><td>GND</td></tr><tr><td>17</td><td>SSTX2-</td><td>18</td><td>SSTX2+</td></tr></table>				1	Vbus	2	D1-	3	D1+	4	GND	5	SSRX1-	6	SSRX1+	7	GND	8	SSTX1-	9	SSTX1+	10	Vbus	11	D2-	12	D2+	13	GND	14	SSRX2-	15	SSRX2+	16	GND	17	SSTX2-	18	SSTX2+
1	Vbus	2	D1-																																					
3	D1+	4	GND																																					
5	SSRX1-	6	SSRX1+																																					
7	GND	8	SSTX1-																																					
9	SSTX1+	10	Vbus																																					
11	D2-	12	D2+																																					
13	GND	14	SSRX2-																																					
15	SSRX2+	16	GND																																					
17	SSTX2-	18	SSTX2+																																					
Type	Standard Dual USB3.0 connector																																							

LAN	LAN1 (29), LAN2 (30)																
PIN Define	 <table border="1"> <tr><td>1</td><td>A+</td></tr> <tr><td>2</td><td>A-</td></tr> <tr><td>3</td><td>B+</td></tr> <tr><td>4</td><td>C+</td></tr> <tr><td>5</td><td>C-</td></tr> <tr><td>6</td><td>B-</td></tr> <tr><td>7</td><td>D+</td></tr> <tr><td>8</td><td>D-</td></tr> </table>	1	A+	2	A-	3	B+	4	C+	5	C-	6	B-	7	D+	8	D-
1	A+																
2	A-																
3	B+																
4	C+																
5	C-																
6	B-																
7	D+																
8	D-																
Type	Standard Single RJ45 Connector																
Memo	Left LED:Orange Right LED:Orange/Green Dual LED Realtek RTL 8111G 10 / 100 / 1000 Mb/s auto negotiation, Half / Full duplex capability																

JEUP	JEUP1(31)											
PIN Define		<table><tr><th>Pin</th><th>1-2</th><th>2-3</th></tr><tr><td>Define</td><td>Enable</td><td>Disale</td></tr><tr><td>Default</td><td colspan="2">1-2</td></tr></table>	Pin	1-2	2-3	Define	Enable	Disale	Default	1-2		
Pin	1-2	2-3										
Define	Enable	Disale										
Default	1-2											
Type	1x3 DuPont Header PH=2.0mm											
Memo	This Setting Combine with Bios Setting When short 1-2 , Bios ERP Setting will take effect When short 2-3 。 Bios ERP Setting Will not take effect.											

LED	LED1 (32)						
PIN Define	 <table border="1"> <tr> <th>Location</th><th>Name</th></tr> <tr> <td>UP</td><td>HDD</td></tr> <tr> <td>DOWN</td><td>WIFI/3G</td></tr> </table>	Location	Name	UP	HDD	DOWN	WIFI/3G
Location	Name						
UP	HDD						
DOWN	WIFI/3G						
Type	Green LED						
Memo	1. Up LED, Indicate HDD status 2. Down LED, Indicate Wifi/3G Module work status						

AUTOPWR	JAT1(33)			
PIN Define		Pin	1-2	2-3
		Define	ATX	AT
		Default	1-2	
Type	1x3 DuPont Header PH=2.0mm			
Memo	This Setting is independent from Bios Power State setting			